



High-Speed Drivers with Dual SPST JFET Switches

FEATURES

- Constant On-Resistance Over Entire Analog Range
- Low Leakage
- Low Crosstalk
- Rad Hardness

BENEFITS

- Low Distortion
- Eliminates Large Signal Errors
- High Precision
- High Bandwidth Capability
- Fault Protection

APPLICATIONS

- Audio Switching
- Video Switching
- Sample/Hold
- Guidance and Control Systems
- Aerospace

DESCRIPTION

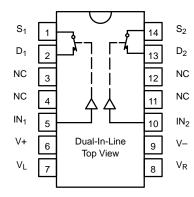
The DG180/181/182 are precision dual single-pole, single-throw (SPST) analog switches designed to provide accurate switching of video and audio signals. This series is ideally suited for applications requiring a constant on-resistance over the entire analog range.

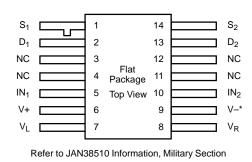
The major difference in the devices is the on-resistance (DG180—10 Ω , DG181—30 Ω , DG182—75 Ω). Reduced errors are achieved through low leakage current (I_{D(on)} < 2 nA). Applications which benefit from the flat JFET

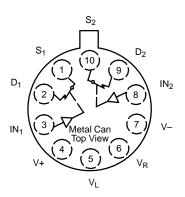
on-resistance include audio switching, video switching, and data acquisition.

To achieve fast and accurate switch performance, each device comprises four n-channel JFET transistors and a TTL compatible bipolar driver. In the on state, each switch conducts current equally well in either direction. In the off condition, the switches will block up to 20 V peak-to-peak, with feedthrough of less than –60 dB at 10 MHz.

FUNCTIONAL BLOCK DIAGRAM AND PIN CONFIGURATION







*Common to Substrate and Case

TRUTH TABLE							
Logic Switch							
0	ON						
1 OFF							

Logic "0" ≤ 0.8 V Logic "1" ≥ 2.0 V



ORDERING INFORMATION								
Temp Range	Package	Part Number						
–25 to 85°C	10-Pin Metal Can	DG181BA						
-23 to 63 C	14-Pin Sidebraze	DG180BP						
		DG180AA/883, 5962-8767301IA						
	10-Pin Metal Can	DG181AA/883, JM38510/11101BIA						
		DG182AA/883, JM38510/11102BIA						
		DG180AP/883, 5962-8767301CA						
–55 to 125°C	14-Pin Sidebraze	DG181AP/883, JM38510/11101BCA						
		DG182AP/883, JM38510/11102BCA						
		5962-8767301XA						
	14-Pin Flat Pack	JM38510/11101BXA						
		JM38510/11102BXA						

ABSOLUTE MAXIMUM RATINGS

V+ to V
V+ to V _D
V_D to V– $$
V_D to V_D $\pm 22 \ V$
V_L to V– $$ 36 V $$
$V_L to V_{\text{IN}} \dots \hspace{1.5cm} 8 V$
$V_LtoV_R\qquad \qquad \qquad 8V$
V_{IN} to V_R
V_{R} to V– $$
V_{R} to V_{IN}
Current (S or D) DG180

Current (S or D) DG181, DG182
Current (All Other Pins)
Storage Temperature –65 to 150°
Power Dissipation ^a
10-Pin Metal Can ^b
14-Pin Sidebraze ^c 825 m\
14-Pin Flat Pack ^d

Notes:

- es.
 All leads welded or soldered to PC Board.
 Derate 6 mW/°C above 75°C
 Derate 11 mW/°C above 75°C
 Derate 10 mW/°C above 75°C
- b.

SCHEMATIC DIAGRAM (TYPICAL CHANNEL)

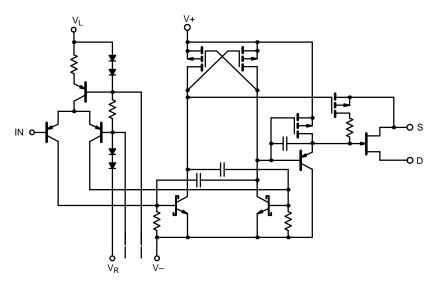


FIGURE 1.



		Test Conditions Unless Specified V+= 15 V, V-= -15 V, V _L = 5 V V _R = 0 V, V _{IN} = 2 V, 0.8 V ^f				A Suffix -55 to 125°C		B Suffix –25 to 85°C		
Parameter	Symbol			Tempb	Тур ^с	Mind	Max ^d	Mind	Max ^d	Unit
Analog Switch	•			•		•	•	•		
Analog Signal Range ^e	V _{ANALOG}			Full		-7.5	15	-7.5	15	V
Drain-Source On-Resistance	r _{DS(on)}	$I_S = -10 \text{ mA}, V_D = -7.5 \text{ V}$		Room Full	7.5		10 20		15 25	Ω
Source Off	1	V _S = ± V+ =	10 V, $V_D = \mp 10 V$ 10 V, $V = -20 V$	Room Hot	0.05		10 1000		15 300	
Leakage Current	IS(off)	V _S = ±	$7.5 \text{ V}, \text{ V}_{\text{D}} = \mp 7.5 \text{ V}$	Room Hot	0.05		10 1000		15 300	1
Drain Off	,	V _S = ± V+ =	10 V, $V_D = \mp 10 V$ 10 V, $V = -20 V$	Room Hot	0.04		10 1000		15 300	nA
Leakage Current	I _{D(off)}	$V_S = \pm 7.5 \text{ V}, V_D = \mp 7.5 \text{ V}$		Room Hot	0.03		10 1000		15 300	
Channel On Leakage Current	I _{D(on)}	$V_D = V_S = \pm 7.5 \text{ V}$		Room Hot	-0.1	-2 -200		-10 -200		
Saturation Drain Current	I _{DSS}	2 ms Pulse Duration		Room	300					mA
Digital Input	_			•						
Input Current with Input Voltage High	I _{INH}		V _{IN} = 5 V		<0.01		10 20		10 20	,
Input Current with Input Voltage Low	I _{INL}		V _{IN} = 0 V	Full	-30	-250		-250		μΑ
Dynamic Characteris	tics			•		•				
Turn-On Time	t _{on}	0 0 "	·	Room	240		400		600	
Turn-Off Time	t _{off}	See Switch	ning Time Test Circuit	Room	140		200		250	ns
Source-Off Capacitance	C _{S(off)}		$V_S = -5 \text{ V}, I_D = 0$	Room	21					
Drain-Off Capacitance	C _{D(off)}	f = 1 MHz	$V_D = -5 \text{ V}, I_S = 0$	Room	17					pF
Channel-On Capacitance	C _{D(on)}	1 - 1 1 1 1 1 1	$V_D = V_S = 0 V$	Room	17					
Off Isolation	OIRR	f = 1	MHz, $R_L = 75 \Omega$	Room	>55					dB
Power Supplies	•			•		•		•		
Positive Supply Current	l+			Room	0.6		1.5		1.5	
Negative Supply Current	I–	1 .		Room	-2.7	-5		-5		1
Logic Supply Current	ΙL	V _{II}	1 = 0 V, or 5 V	Room	3		4.5		4.5	mA
Reference Supply Current	I _R			Room	-1	-2		-2		1

- Refer to PROCESS OPTION FLOWCHART.

 Room = 25°C, Full = as determined by the operating temperature suffix.

 Typical values are for DESIGN AID ONLY, not guaranteed nor subject to production testing.
- The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- Guaranteed by design, not subject to production test. $V_{\rm IN}$ = input voltage to perform proper function.



		Test Conditions Unless Specified V+ = 15 V, V- = -15 V, V _L = 5 V V _R = 0 V, V _{IN} = 2 V, 0.8 V ^f				A Suffix -55 to 125°C		B Suffix –25 to 85°C		
Parameter	Symbol			Tempb	Турс	Mind	Max ^d	Min ^d	Max ^d	Unit
Analog Switch	•			•		•	•			•
Analog Signal Range ^e	V _{ANALOG}			Full		-7.5	15	-7.5	15	V
Drain-Source On-Resistance	r _{DS(on)}	$I_S = -10 \text{ mA}, V_D = -7.5 \text{ V}$		Room Full	18		30 60		50 75	Ω
Source Off		$V_S = \pm 10 \text{ V}, V_D = \mp 10 \text{ V}$ $V_T = 10 \text{ V}, V_T = -20 \text{ V}$		Room Hot	0.05		1 100		5 100	
Leakage Current	IS(off)	V _S = ±7	7.5 V, V _D = ∓7.5 V	Room Hot	0.07		1 100		5 100	
Drain Off		V _S = ± V+ = '	10 V, $V_D = \mp 10 V$ 10 V, $V = -20 V$	Room Hot	0.5		1 100		5 100	nA
Leakage Current	ID(off)	$V_S = \pm 7.5 \text{ V}, V_D = \mp 7.5 \text{ V}$		Room Hot	0.6		1 100		5 100	1
Channel On Leakage Current	I _{D(on)}	$V_D = V_S = \pm 7.5 \text{ V}$		Room Hot	-0.02	-2 -200		-10 -200		1
Digital Input	•			•			•	•		•
Input Current with Input Voltage High	I _{INH}	V _{IN} = 5 V		Room Hot	<0.01		10 20		10 20	
Input Current with Input Voltage Low	I _{INL}		V _{IN} = 0 V	Full	-30	-250		-250		μΑ
Dynamic Characteristic	s									
Turn-On Time	t _{on}			Room	85		150		180	
Turn-Off Time	t _{off}	See Switch	ning Time Test Circuit	Room	95		130		150	ns
Source-Off Capacitance	C _{S(off)}		$V_S = -5 \text{ V}, I_D = 0$	Room	9					
Drain-Off Capacitance	C _{D(off)}	f = 1 MHz	$V_D = -5 \text{ V}, I_S = 0$	Room	6					pF
Channel-On Capacitance	C _{D(on)}		$V_D = V_S = 0 V$	Room	14					1
Off Isolation	OIRR	f = 1	MHz, $R_L = 75 \Omega$	Room	>50					dB
Power Supplies	•						•	•		
Positive Supply Current	l+			Room	0.6		1.5		1.5	
Negative Supply Current	I-	1		Room	-2.7	-5		- 5		1.
Logic Supply Current	IL	\vert_N	₁ = 0 V, or 5 V	Room	3.1		4.5		4.5	mA
Reference Supply Current	I _R	1		Room	-1	-2		-2		1

- Refer to PROCESS OPTION FLOWCHART.
- Room = 25°C, Full = as determined by the operating temperature suffix.

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 V_{IN} = input voltage to perform proper function.



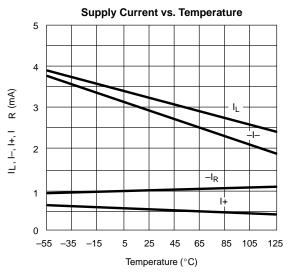
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Parameter	Symbol			Tempb	Тур ^с	Min ^d	Max ^d	Min ^d	Max ^d	Unit	
Analog Switch											
Analog Signal Range ^e	V _{ANALOG}			Full		-10	15	-10	15	V	
Drain-Source On-Resistance	r _{DS(on)}	I _S = -1	$I_S = -10 \text{ mA}, V_D = -7.5 \text{ V}$		35		75 150		100 150	Ω	
Source Off		V _S = ± V+ =	$= 10 \text{ V}, \text{ V}_{\text{D}} = \mp 10 \text{ V}$ $= 10 \text{ V}, \text{ V}_{\text{D}} = -20 \text{ V}$	Room Hot	0.05		1 100		5 100		
Leakage Current	^I S(off)	V _S = ±	= 10 V, V _D = ∓10 V	Room Hot	0.07		1 100		5 100	1	
Drain Off	1	$V_S = \pm 10 \text{ V}, V_D = \mp 10 \text{ V}$ V+ = 10 V, V- = -20 V		Room Hot	0.4		1 100		5 100	nA	
Leakage Current	ID(off)	$V_S = \pm 10 \text{ V}, V_D = \mp 10 \text{ V}$		Room Hot	0.5		1 100		5 100		
Channel On Leakage Current	I _{D(on)}	$V_D = V_S = \pm 10 \text{ V}$		Room Hot	-0.02	-2 -200		-10 -200			
Digital Input					•			•			
Input Current with Input Voltage High	I _{INH}		V _{IN} = 5 V		<0.01		10 20		10 20		
Input Current with Input Voltage Low	I _{INL}		V _{IN} = 0 V	Full	-30	-250		-250		μА	
Dynamic Characterist	ics										
Turn-On Time	t _{on}	0 0 1	1. T. T. T. (0)	Room	120		250		300		
Turn-Off Time	t _{off}	See Switch	ching Time Test Circuit	Room	100		130		150	ns	
Source-Off Capacitance	C _{S(off)}		$V_S = -5 \text{ V}, I_D = 0$	Room	9						
Drain-Off Capacitance	C _{D(off)}	f = 1 MHz	$V_D = -5 \text{ V}, I_S = 0$	Room	6					pF	
Channel-On Capacitance	C _{D(on)}		$V_D = V_S = 0 V$	Room	14					1	
Off Isolation	OIRR	f = 1	MHz, $R_L = 75 \Omega$	Room	>50					dB	
Power Supplies	•				•	•	•	•			
Positive Supply Current	l+			Room	0.6		1.5		1.5		
Negative Supply Current	I–	V _{IN} = 0 V, or 5 V		Room	-2.7	-5		- 5		1 .	
Logic Supply Current	ΙL			Room	3.1		4.5		4.5	mA	
Reference Supply Current	I _R			Room	-1	-2	 	-2	-	1	

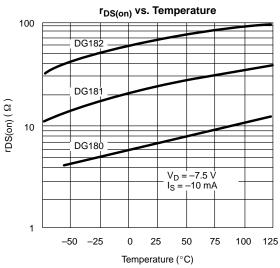
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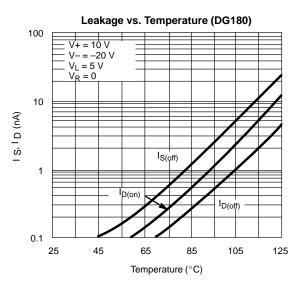
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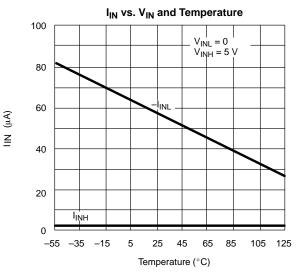


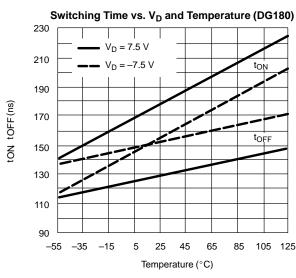
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

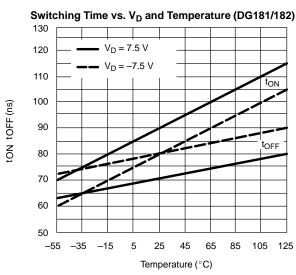








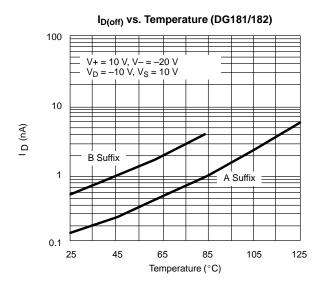




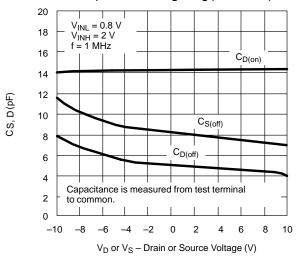




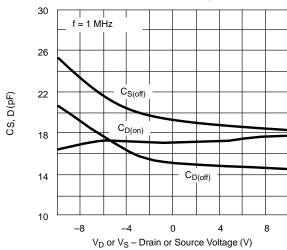
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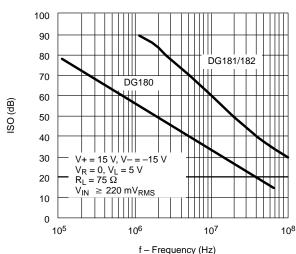
Capacitance vs. V_D or V_S (DG181/182)



Capacitance vs. V_D or V_S (DG180)



Off Isolation vs. Frequency





TEST CIRCUITS

Feedthrough due to charge injection may result in spikes at the leading and trailing edge of the output waveform.

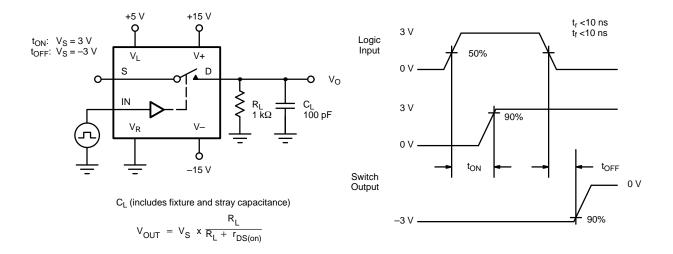


FIGURE 2. Switching Time

APPLICA	APPLICATION HINTS ^a											
Switch	V+ Positive Supply Voltage (V)	V– Negative Supply Voltage (V)	V _L Logic Supply Voltage (V)	V _R Reference Supply Voltage (V)	V _{IN} Logic Input Voltage V _{INH(min)} /V _{INL(max}) (V)	V _S Analog Voltage Range (V)						
	15 ^b	-15	5	GND	2.0/0.8	–7.5 to 15						
DG180 DG181	10	-20	5	GND	2.0/0.8	-12.5 to 10						
20101	12	-12	5	GND	2.0/0.8	-4.5 to 12						
	15 ^b	-15	5	GND	2.0/0.8	–10 to 15						
DG182	10	-20	5	GND	2.0/0.8	-15 to 10						
	12	-12	5	GND	2.0/0.8	–7 to 12						

<sup>a. Application Hints are for DESIGN AID ONLY, not guaranteed and not subject to production testing.
b. Electrical Parameter Chart based on V+ = 15 V, V_L = 5 V, V_R = GND</sup>



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