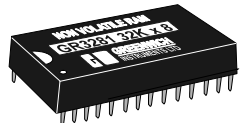


GR3281 (32K x 8) NON-VOLATILE RAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Min	Max	Units
Vdd	-0.3	7.0	Volts
V _{i/o}	-0.3	Vdd +0.3	Volts
Temp	-20	+70	deg. C

DESCRIPTION

The GR3281 is a 32768 word by 8 bits (32K x 8) non-volatile CMOS Static Ram, fabricated from advanced silicon gate CMOS technology and a high reliability lithium power cell.

The pin-out of the GR3281 conforms to the JEDEC standards and is fully compatible with normal static RAM.

The power down circuit is fully automatic and is referenced at 4.5 volts. At this point the GR3281 is write protected by an internal inhibit function for Data Protection and the memory contents are retained by the lithium power source.

Power down is very fast, this being essential for data integrity, taking a maximum of 15 μS (15 microseconds) to power down from 5 volts to 0 volts. This is much faster than system power failure conditions. Therefore there are no special conditions required when installing the GR3281.

The GR3281 can, without external power, retain data almost indefinitely. The limiting factor will be the shelf life of the lithium cell, which is typically ten years. It is possible that this figure may be extended in view of the extremely light duty imposed upon the cell.

APPLICATION

When powered down, the GR3281 is transportable and data can be moved from system to system, this makes it ideal for program development, data collection in data loggers, program changes in process control, automation and robotics and user definable lookup tables, etc.

DISPOSAL INSTRUCTIONS

Do not dispose of non-volatile memory devices by incineration or crushing. Devices may be returned carriage paid to Greenwich Instruments Ltd., for disposal.

UK

Greenwich Instruments Ltd.,
Meridian House, Park Road,
Swanley, Kent. BR8 8AH

Tele: 08700 505 404
Fax: 08700 505 405

Greenwich Instruments Ltd., are continually developing their products and reserve the right to alter specifications without prior notice. Standard Terms and Conditions of Sale apply.

OPERATING CONDITIONS

Symbol	Min	Typ	Max	Unit
Vdd	4.75	5.0	5.5	Volts
V _{in} (1)	2.2			Volts
V _{in} (0)			0.8	Volts
I _{in} (any other pin)	-1.0		+1.0	μA
V _{out} (1)(I _{out} = -1mA)	2.4			Volts
V _{out} (0)(I _{out} = +2mA)			0.4	Volts
I _{dd} (Active)		30		mA
I _{dd} (Deselected)		1.0		mA
T _{cycle}			100	nS
C _{in} (any pin)		10		pF

OPERATING MODE

CE	OE	WR	MODE	OUTPUT	I _{dd}
H	X	X	Unsel.	Hi-Z	Deselected
L	H	H	Unsel.	Hi-Z	Active
L	L	H	Read	Dout	Active
L	X	L	Write	Din	Active

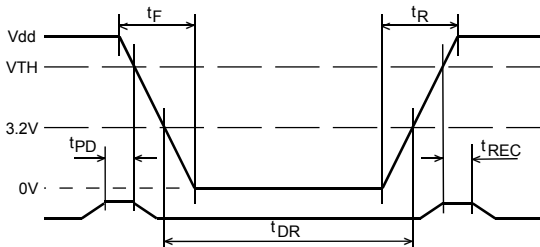
PIN CONNECTIONS

A14	1	28	Vdd
A12	2	27	WR
A7	3	26	A13
A6	4	25	A8
A5	5	24	A9
A4	6	23	A11
A3	7	22	OE
A2	8	21	A10
A1	9	20	CE
A0	10	19	D7
D0	11	18	D6
D1	12	17	D5
D2	13	16	D4
GND	14	15	D3

PIN DESIGNATIONS

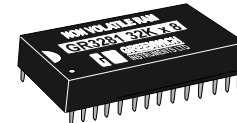
Pin	Function
A0-A12	Address I/P's
D0-D7	Data in/out
OE	Output Enable
CE	Chip Enable
WR	Write Enable
Vdd	+5Volt Power
GND	Ground

DATA RETENTION OPERATING CONDITIONS



Symbol	Parameter	Min	Typ	Max	Units
Vdd	Operating supply voltage	4.75	5.0	5.50	Volts
VTH	Data retention voltage		4.5		Volts
t _F	Vdd slew to 0V	15			μS
t _R	Vdd slew 0V to 5.0V	15			μS
t _{REC}	CE to O/P valid from power up			15	μS
t _{DR}	Data retention time		10		Years
t _{PD}	CE at Vin(1) before power down	0			μS

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I _{dd} (Deselected)		1.0		mA
T _{cycle}			100	nS
C _{in} (any pin)		10		pF

OPERATING MODE

CE	OE	WR	MODE	OUTPUT	I _{dd}
H	X	X	Unsel.	Hi-Z	Deselected
L	H	H	Unsel.	Hi-Z	Active
L	L	H	Read	Dout	Active
L	X	L	Write	Din	Active

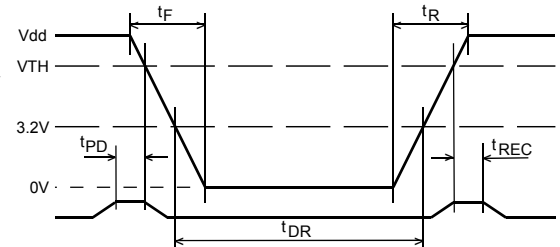
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A0	10	19	D7
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D1	12	17	D5
D2	13	16	D4
GND	14	15	D3

PIN DESIGNATIONS

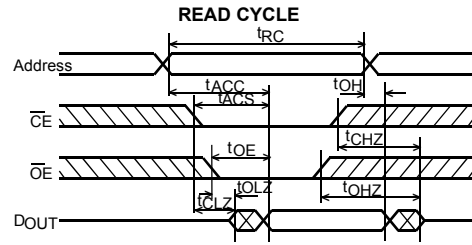
Pin	Function
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D0-D7	Data in/out
OE	Output Enable
CE	Chip Enable
WR	Write Enable
Vdd	+5Volt Power
GND	Ground

DATA RETENTION OPERATING CONDITIONS



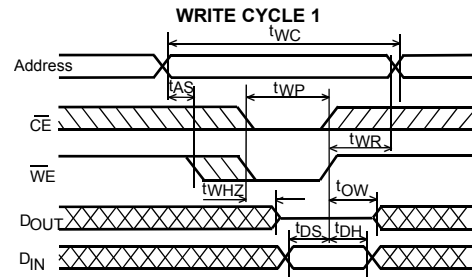
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**GR3281 (32K x 8)
NON-VOLATILE RAM**

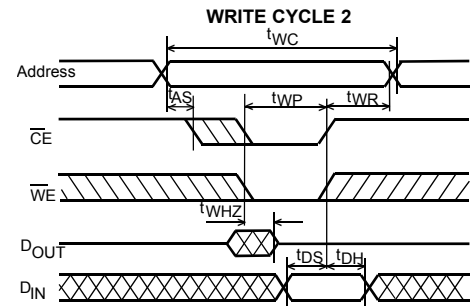


TIMING (nS-nano seconds)

Read Cycle		100nS	
Symbol	Parameter	Min	Max
tRC	Read cycle time	100	
tACC	Access time		100
tACS	CE to output valid		100
tOE	OE to output valid		50
tCLZ	CE to output active	10	
tOLZ	OE to output active	10	
tOH	Output hold time	20	
tCHZ	CE to output disable		35
tOHZ	OE to output disable		35



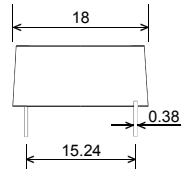
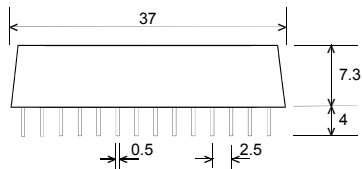
Write Cycle		100nS	
Symbol	Parameter	Min	Max
tWC	Write cycle time	100	
tWP	Write pulse width	60	
tAS	Address setup time	0	
tWR	Write recovery time	0	35
tWHZ	WR to output disable		35
tOW	Output active from WR	10	
tDS	Data setup time	35	
tDH	Data HOLD TIME	0	



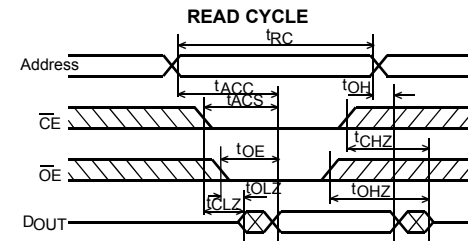
- Notes
1. WE must be high during address transitions.
 2. A Write occurs during the overlap of a low CE and a low WE.
 3. WE is high for a read cycle.

REPLACES
62256., 43256., 55257., etc.

DIMENSIONS (mm)

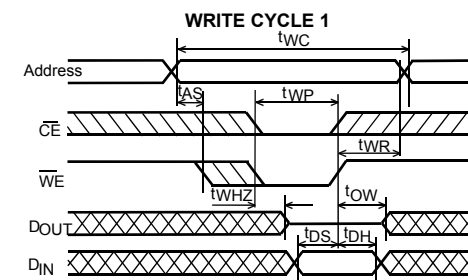


**GR3281 (32K x 8)
NON-VOLATILE RAM**

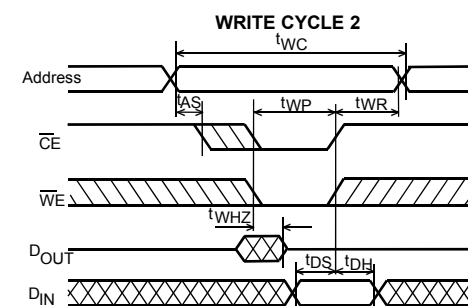


TIMING (nS-nano seconds)

Read Cycle		100nS	
Symbol	Parameter	Min	Max
tRC	Read cycle time	100	
tACC	Access time		100
tACS	CE to output valid		100
tOE	OE to output valid		50
tCLZ	CE to output active	10	
tOLZ	OE to output active	10	
tOH	Output hold time	20	
tCHZ	CE to output disable		35
tOHZ	OE to output disable		35



Write Cycle		100nS	
Symbol	Parameter	Min	Max
tWC	Write cycle time	100	
tWP	Write pulse width	60	
tAS	Address setup time	0	
tWR	Write recovery time	0	35
tWHZ	WR to output disable		35
tOW	Output active from WR	10	
tDS	Data setup time	35	
tDH	Data HOLD TIME	0	



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1. WE must be high during address transitions.
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