SPEC. No.	TQ3C-8EAC0-E1CWJ26-00
DATE	April 26, 2004

S P E C

FOR : _____

TYPE : KHS072VG1AB-G01

CONTENTS

Issued
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🔷 KYOCERA
Hayato LCD Division

KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

This specification is subject to change without notice.
Consult Kyocera before ordering.

Original	Designed by :Engineering Dept.			Confirmed by :QA Dept.	
Issue Data	Prepared	Checked	Approved	Checked	Approved
April 26, 2004	Y. Yonozaki	K. Joy	H. Ohno	Y. Joshida	S. Hayashi

Caution

1. This Kyocera LCD module has been specifically designed for use only in electronic devices in the areas of audio control, office automation, industrial control, home appliances, etc. The modules should not be used in applications where module failure could result in physical harm or loss of life, and Kyocera expressly disclaims any and all liability relating in any way to the use of the module in such applications.

2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, losses, damages, liabilities, awards, costs, and expenses, including legal fees, resulting from or arising out of Customer's use, or sale for use, of Kyocera modules in applications.

3. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

Date	Designed by: Engineering Dept.			Confirmed by: QA Dept.	
	Prepared	Checked	Approved	Checked	Approved

Rev. No.	Date	Page	Descriptions

Designed by: Engineering Dept.   Confirmed by: QA Dept.

1. Application

This data sheet defines the specification for a (640×R.G.B)×480 dot, STN Transflective color dot matrix type Liquid Crystal Display with CFL backlight.

2. Construction and Outline
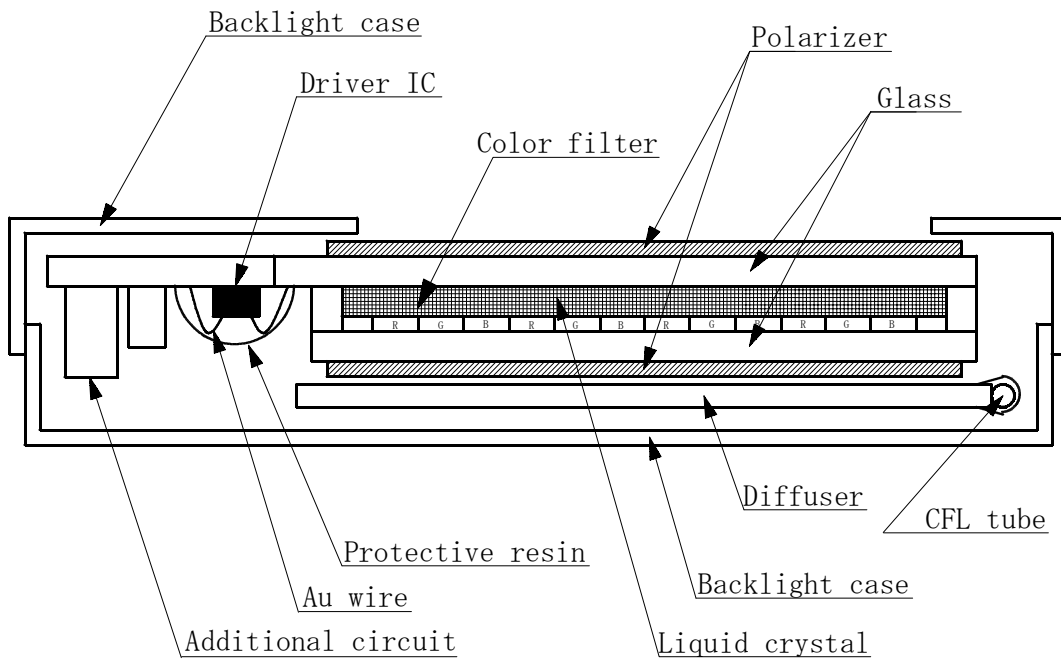
(640×R.G.B)×480 dots, COB type LCD with CFL backlight.

Backlight system   : Side-edge type CFL (1 tube).

Inverter   : Option.
                      Recommended Inverter : PH-BLC08-K2 (HITACHI MEDIA ELECTRONICS)
                      or equivalent.

Polarizer   : Glare treatment.

Additional circuit : Bias voltage circuit, Randomizing circuit, DC-DC converter

This drawing is showing conception only.

3. Mechanical Specifications

ITEM	SPECIFICATION	UNIT
Outline dimensions	179.3 (W) × 130.5 (H) × 6 (D)	mm
Effective viewing area	147.9 (W) × 111.4 (H)	mm
Dot number	(640×R.G.B) (W) × 480 (H)	Dots
Dot size	0.056 (W) × 0.208 (H)	mm
Dot pitch	0.076 (W) × 0.228 (H)	mm
Display color *1	White *2	—
Base color *1	Black *2	—
Mass	170	g

*1 Due to the characteristics of the LC material, the color vary with environmental temperature.

*2 Negative-type display

     Display data "H" :R.G.B Dots ON : White
     Display data "L" :R.G.B Dots OFF : Black

4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

ITEM	SYMBOL	MIN.	MAX.	UNIT
Supply voltage for logic	VDD	0	6.0	V
Supply voltage for LCD driving	VCONT	0	VDD	V
Input Signal voltage *1	Vin	0	VDD+0.3	V

   *1 Input signal :CP, LOAD, FRM, DISP, HD0～HD7, LD0～LD7

4-2. Environmental absolute maximum ratings

ITEM	SYMBOL	MIN	MAX	UNIT
Operating temperature *1	Top	0	50	℃
Storage temperature *2	T_{STO}	−20	60	℃
Operating humidity *3	Hop	10	*4	%RH
Storage humidity *3	H_{STO}	10	*4	%RH
Vibration	—	*5	*5	—
Shock	—	*6	*6	—

*1   LCD's display quality shall not be guaranteed at the temperature range of :
     below 0℃ and upper 40℃.

*2   Temp. = −20℃ ＜ 48 h ,   Temp = 60℃ ＜ 168 h
     Store LCD panel at normal temperature/humidity. Keep it free from vibration and shock.
     LCD panel that is kept at low or high temperature for a long time can be defective due to
     the other conditions, even if the temperature satisfies standard.
     (Please refer to 16. Precautions for use as detail).

*3   Non-condensation.

*4   Temp. ≦ 40℃, 85% RH Max.
     Temp. ＞ 40℃, Absolute Humidity shall be less than 85%RH at 40℃.

*5

Frequency	10～55 Hz	Converted to acceleration value : $(0.3～9 \ m/s^2)$
Vibration width	0.15 mm	
Interval	10-55-10 Hz 1 minute	

     2 hours in each direction X/Y/Z (6 hours as total)
     EIAJ ED-2531

*6   Acceleration: 490 m/s^2
     Pulse width :   11 ms
     3 times in each direction :   ±X/±Y/±Z.
     EIAJ ED-2531

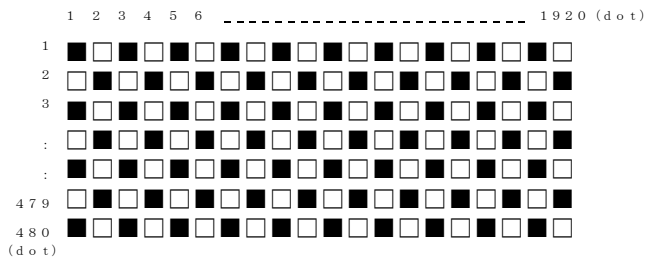## 5. Electrical Characteristics

5-1. VDD = 3.3V

VDD = +3.3V ± 0.3V, Temp. = 0～50℃

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply voltage for logic	VDD	—	3.0	3.3	3.6	V
LCD driving voltage　*1	Vop = VCONT	0 ℃	(0.80)	—	—	V
		25 ℃	(1.35)	(1.95)	(2.55)	V
		50 ℃	—	—	(2.80)	V
Input voltage	Vin	"H" level	0.8VDD	—	VDD	V
		"L" level	0	—	0.2VDD	V
Clock frequency	f cp		4.03	4.32	10.0	MHz
Frame frequency　　*2	f FRM		70	75	80	Hz
Current consumption for logic	IDD	—	—	(126)	(189)	mA
Power consumption	Pdisp		—	(416)	(624)	mW

*1 Maximum contrast ratio is obtained by adjusting the LCD supply voltage (Vop = VCONT) for driving LCD.

*2 In consideration of display quality, it is recommended that frame frequency is set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency values: Generally, as frame and clock frequencies become higher current consumption will get bigger and display quality will be degraded.

*3 Display high frequency pattern, (see below).
VDD = 3.3V , Vop = VCONT , f FRM = 75 Hz , fcp = 4.32MHz , Temp. = 25℃
Pattern:

5-2. VDD = 5.0V

VDD = +5.0V ± 5%, Temp. = 0～50℃

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Supply voltage for logic	VDD	—	4.75	5.0	5.25	V
LCD driving voltage *1	Vop=VCONT	0℃	(0.80)	—	—	V
		25 ℃	(1.35)	(1.95)	(2.55)	V
		50 ℃	—	—	(2.80)	V
Input voltage	Vin	"H" level	0.8VDD	—	VDD	V
		"L" level	0	—	0.2VDD	V
Clock frequency	f cp		4.03	4.32	16.0	MHz
Frame frequency *2	f $_{FRM}$		70	75	80	Hz
Current consumption for logic	IDD	*3	—	(86)	(129)	mA
Power consumption	Pdisp		—	(430)	(645)	mW

*1 Maximum contrast ratio is obtained by adjusting the LCD supply voltage (Vop = VCONT) for driving LCD.

*2 In consideration of display quality, it is recommended that frame frequency is set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performance and quality prior to finalizing the frequency values: Generally, as frame and clock frequencies become higher, current consumption will get bigger and display quality will be degraded.

*3 Display high frequency pattern, (see below).
VDD = 5.0V , Vop = VCONT , f $_{FRM}$ = 75 Hz , fcp = 4.32MHz , Temp. = 25℃
Pattern:

6. Optical Characteristics

6-1. Reflective mode

Measuring Spot = φ6mm , Temp. = 25℃

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Response Time	Rise	Tr	$\theta=\phi=\theta$	—	(200)	(300)	ms
	Down	Td	$\theta=\phi=\theta$	—	(150)	(250)	ms
Contrast ratio		CR	$\theta=\phi=\theta$	(2.0)	(5.0)	—	—
Reflectance		ρ	—	(2.5)	(5.0)	—	%

Optimum contrast is obtained by adjusting the LCD driving voltage(Vop) while at the viewing
angle of $\theta = \phi = 0°$.

6-2. Transmissive mode

Measuring Spot = φ6mm , Temp. = 25℃

ITEM		SYMBOL	CONDITION		MIN.	TYP.	MAX.	UNIT
Response time	Rise	Tr	$\theta=\phi=0°$		—	(200)	(300)	ms
	Down	Td	$\theta=\phi=0°$		—	(150)	(250)	ms
Viewing angle range		θ	CR≧2	$\phi=0°$	(−30)	—	(40)	deg.
		φ		$\theta=0°$	(−50)	—	(50)	deg.
Contrast ratio		CR	$\theta=\phi=0°$		(12.5)	(25.0)	—	—
Brightness		L	IL=5.0mA		(45.0)	(90.0)	—	cd/m²
Chromaticity coordinates	Red	x	$\theta=\phi=0°$		(0.40)	(0.45)	(0.50)	—
		y			(0.25)	(0.30)	(0.35)	
	Green	x	$\theta=\phi=0°$		(0.25)	(0.30)	(0.35)	
		y			(0.39)	(0.44)	(0.49)	
	Blue	x	$\theta=\phi=0°$		(0.12)	(0.17)	(0.22)	
		y			(0.11)	(0.16)	(0.21)	
	White	x	$\theta=\phi=0°$		(0.25)	(0.30)	(0.35)	
		y			(0.27)	(0.32)	(0.37)	

Optimum contrast is obtained by adjusting the LCD driving voltage(Vop) while at the viewing
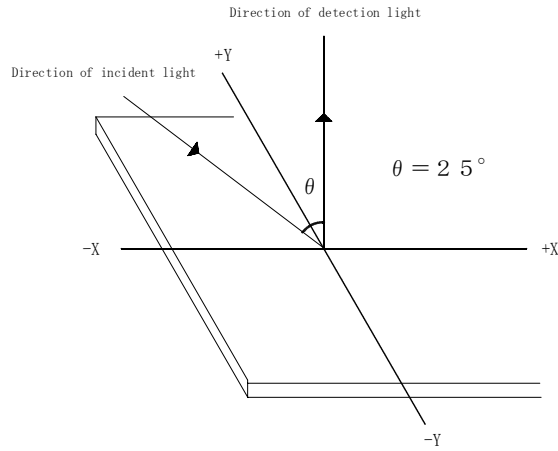angle of $\theta = \phi = 0°$.

6-3. Definition of Vop
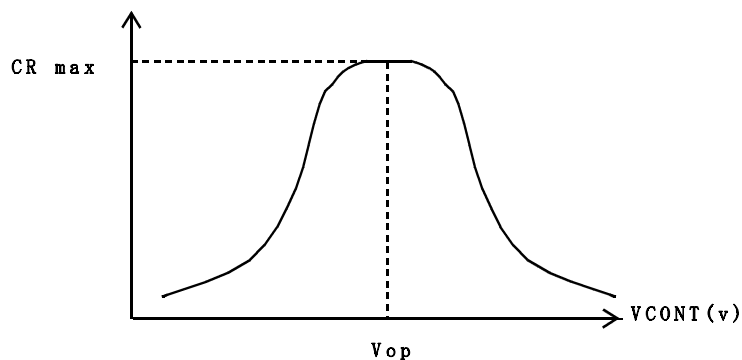
6-4. Definition of response time
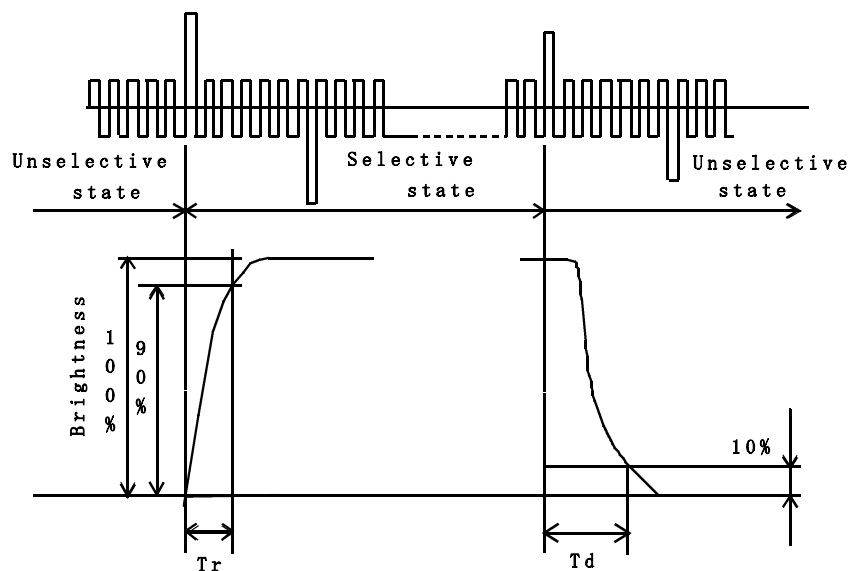
6-5. Definition of viewing angle

(θ direction)   (φ direction)
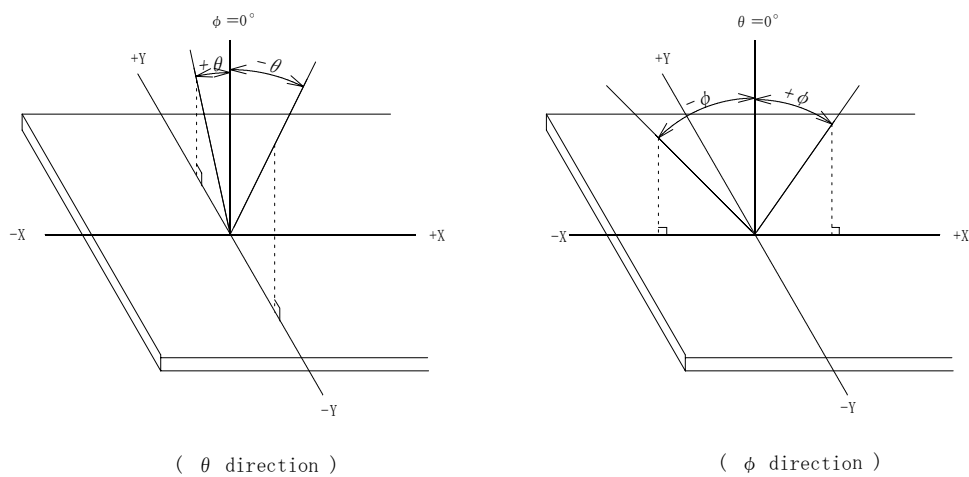
6-6. Measurement method of reflectance

6-7. Definition of Contrast (Reflective Mode)

$$CR(Contrast) = \frac{Reflection\ Brightness\ at\ all\ pixels\ "White"}{Reflection\ Brightness\ at\ all\ pixels\ "Black"}$$
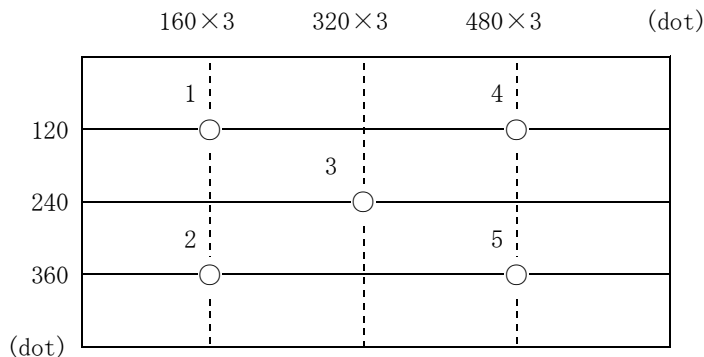
6-8. Definition of Contrast (Transmissive Mode)

$$CR(Contrast) = \frac{Brightness\ at\ all\ pixels\ "White"}{Brightness\ at\ all\ pixels\ "Black"}$$

6-9. Definition of Reflectance:

$$\rho\ (Reflectance) = \frac{Measured\ Reflection\ Brightness}{Reflection\ Brightness\ against\ Standard\ White\ Board} \times 100(\%)$$

6-10. Measuring points

1) Rating is defined as the average brightness inside the viewing area.
2) 30 minutes after CFL is turned on. (Ambient Temp. = 25℃)
3) The inverter should meet the eccentric conditions;
   -Sine, symmetric waveform without spike in positive and negative.
4) Measuring Inverter : PH-BLC08-K2(HITACHI MEDIA ELECTRONICS)

7. Circuit Block Diagram
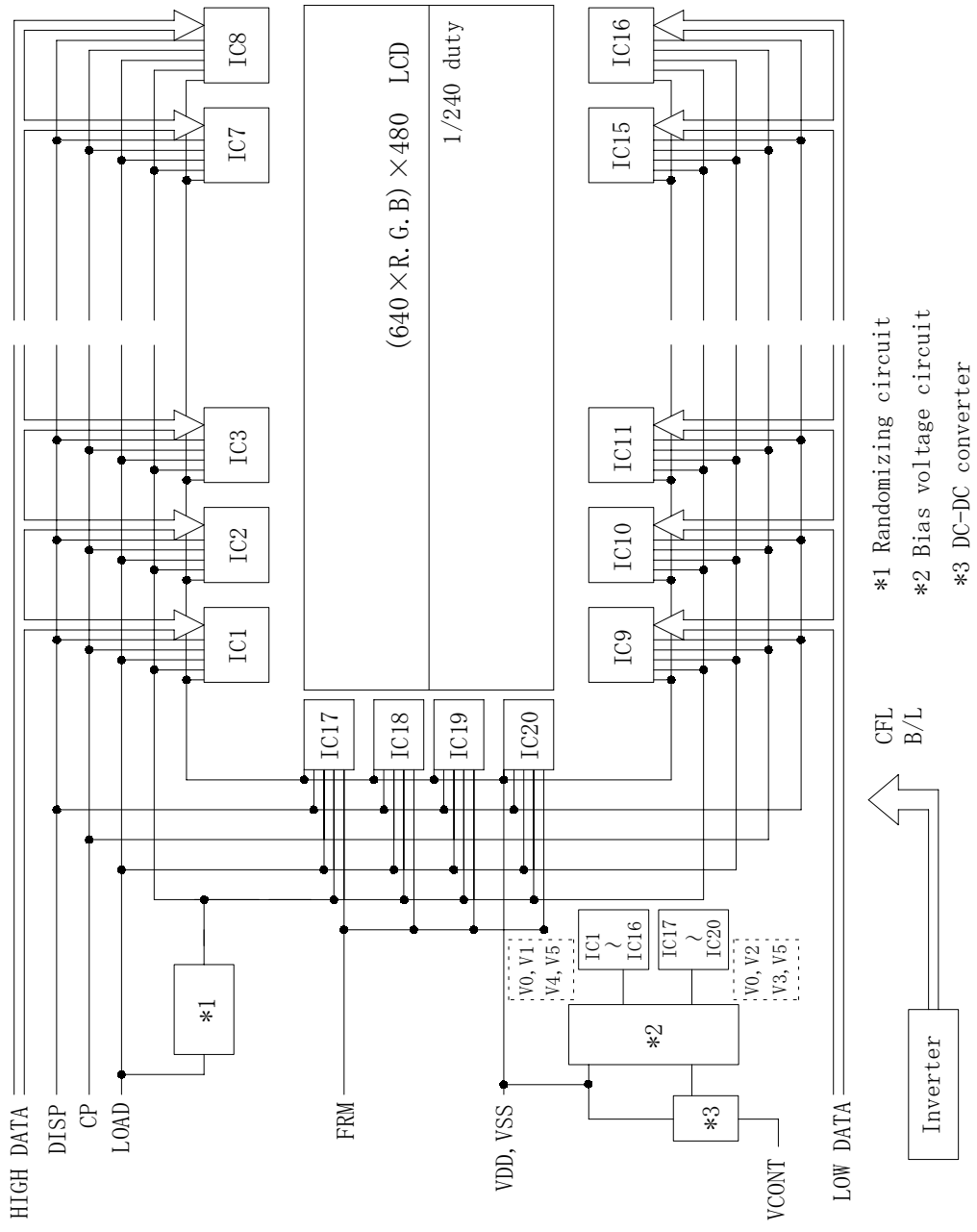
7-1. Power supply
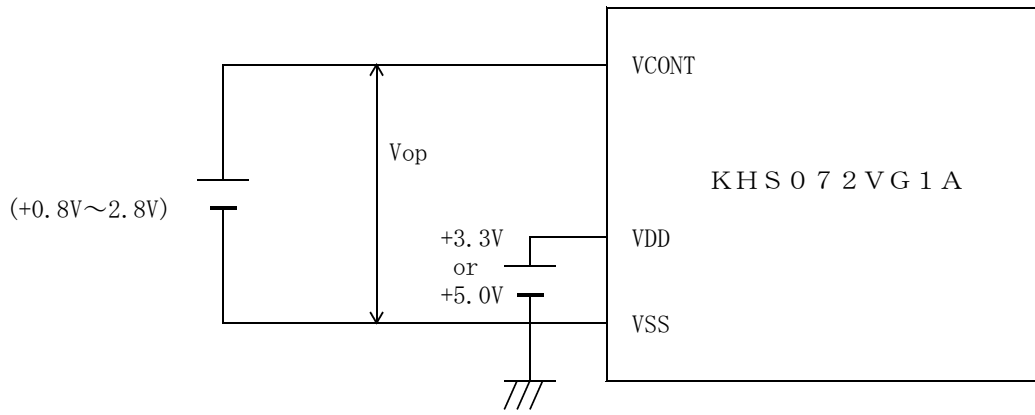
8. Interface signals

8-1. LCD

PIN No.	SYMBOL	DESCRIPTION	LEVEL
1	LD4	Display data (Lower column)	H(ON),L(OFF)
2	VSS	GND	—
3	LD5	Display data (Lower column)	H(ON),L(OFF)
4	FRM	Synchronous signal for driving scanning line	H
5	LD6	Display data (Lower column)	H(ON),L(OFF)
6	LOAD	Data signal latch clock	H → L
7	LD7	Display data (Lower column)	H(ON),L(OFF)
8	VSS	GND	—
9	VSS	GND	—
10	CP	Data signal shift clock	H → L
11	LD0	Display data (Lower column)	H(ON),L(OFF)
12	VCONT	LCD adjust voltage	—
13	LD1	Display data (Lower column)	H(ON),L(OFF)
14	VDD	Power supply for logic	—
15	VSS	GND	—
16	VDD	Power supply for logic	—
17	LD2	Display data (Lower column)	H(ON),L(OFF)
18	DISP	Display control signal	H(ON),L(OFF)
19	LD3	Display data (Lower column)	H(ON),L(OFF)
20	NC	No connect	—
21	VSS	GND	—
22	HD3	Display data (Upper column)	H(ON),L(OFF)
23	HD4	Display data (Upper column)	H(ON),L(OFF)
24	HD2	Display data (Upper column)	H(ON),L(OFF)
25	HD5	Display data (Upper column)	H(ON),L(OFF)
26	HD1	Display data (Upper column)	H(ON),L(OFF)
27	VSS	GND	—
28	HD0	Display data (Upper column)	H(ON),L(OFF)
29	HD6	Display data (Upper column)	H(ON),L(OFF)
30	VSS	GND	—
31	HD7	Display data (Upper column)	H(ON),L(OFF)

```
LCD side connector            : DF9B-31P-1V (HIROSE)
Recommended matching connector : DF9B-31S-1V (HIROSE)
```
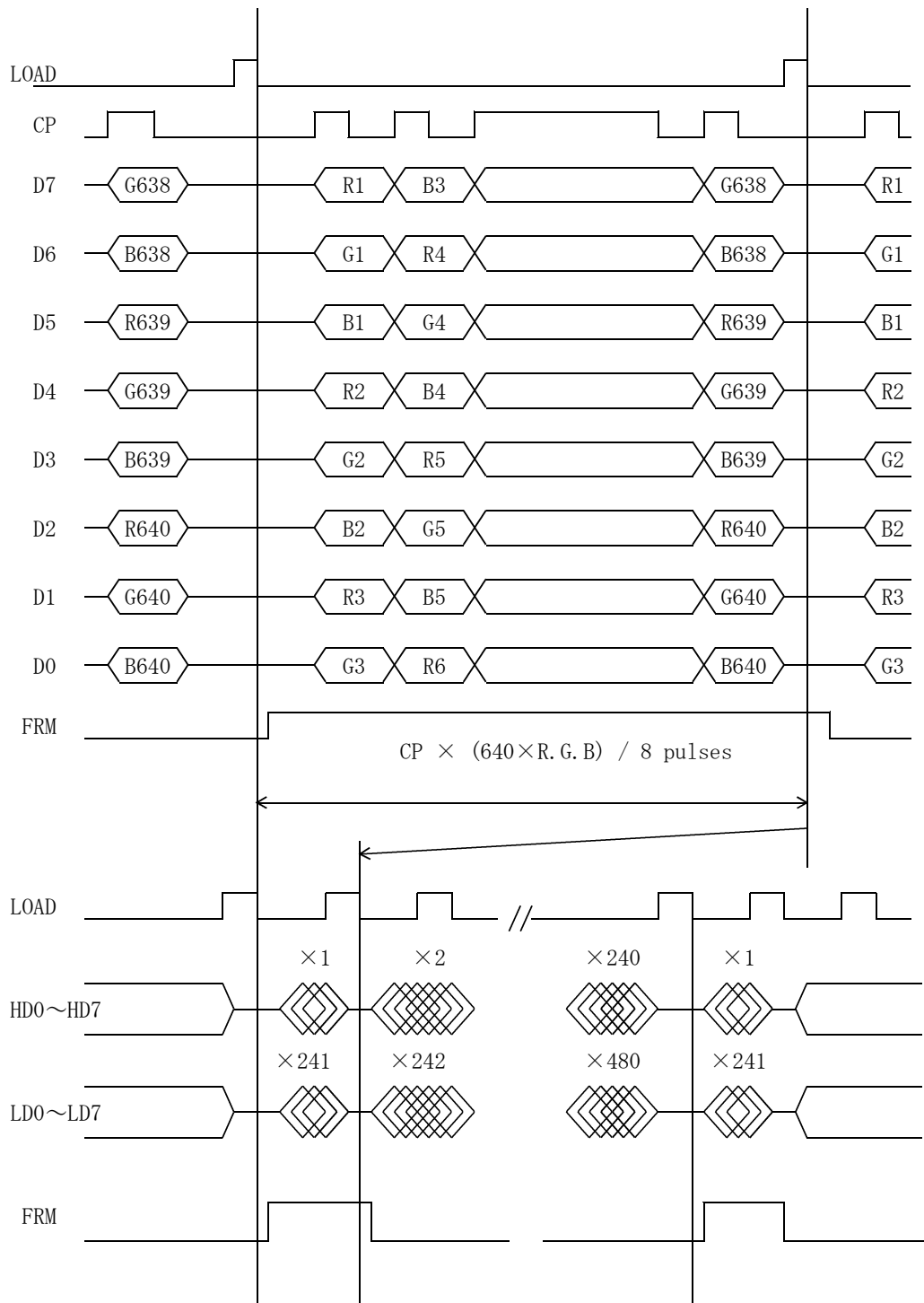
8-2. CFL

PIN No.	SYMBOL	DESCRIPTION
1	HOT	Inverter output high voltage side
2	NC	—
3	COLD	Inverter output low voltage side

```
LCD side connector            : BHR-03VS-1        (JST)
Recommended matching connector : SM02-(8.0)B-BHS-1  (JST)
```
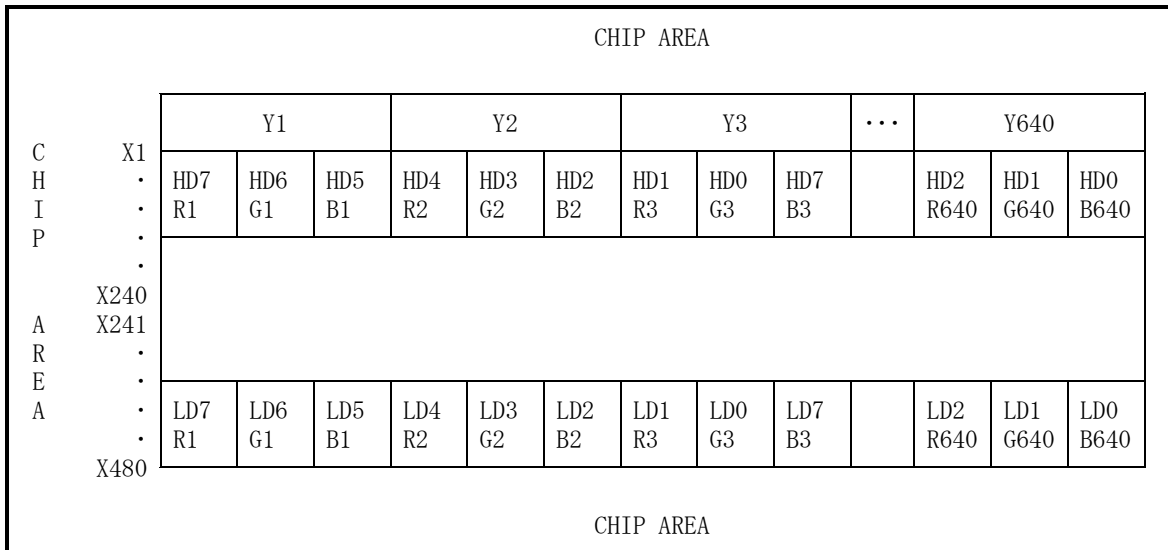
9. Interface Timing Chart

CP × (640×R.G.B) / 8 pulses

| | ×1 | ×2 | ×240 | ×1 |
| | | | | |

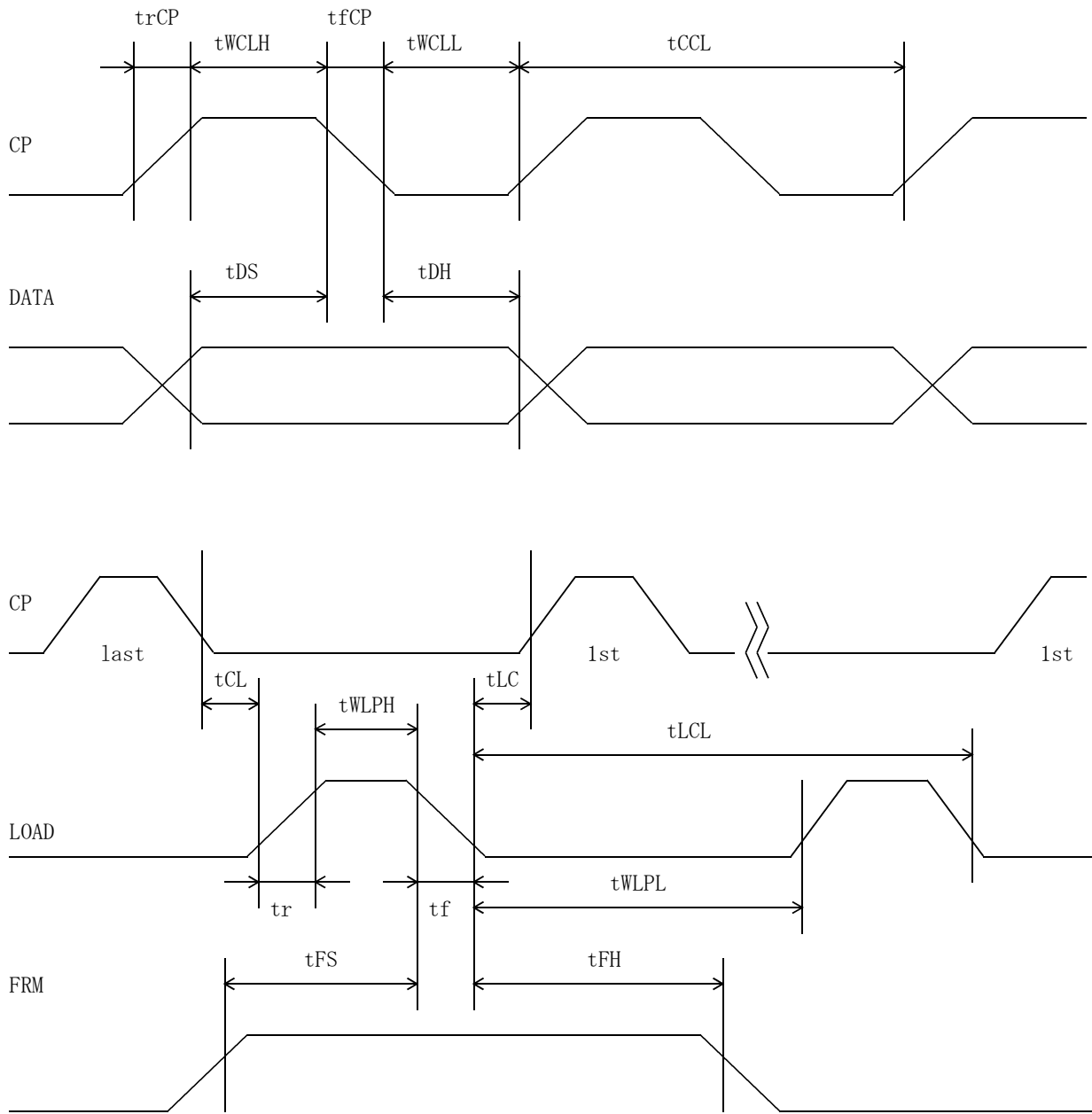| | ×241 | ×242 | ×480 | ×241 |
| | | | | |

* The cycle of load signal should be stable and continuously applied without interruption.
* The above-mentioned timing chart shows a reference to set up a LCD module, not an electrical rating.

10. Data and Screen

		Y1			Y2			Y3			...	Y640		
C H I P	X1 · · ·	HD7 R1	HD6 G1	HD5 B1	HD4 R2	HD3 G2	HD2 B2	HD1 R3	HD0 G3	HD7 B3		HD2 R640	HD1 G640	HD0 B640
P	· X240 X241													
A R E A	· · · · X480	LD7 R1	LD6 G1	LD5 B1	LD4 R2	LD3 G2	LD2 B2	LD1 R3	LD0 G3	LD7 B3		LD2 R640	LD1 G640	LD0 B640

CHIP AREA (top)

CHIP AREA (bottom)

11. Input Timing Characteristics

11-1. Switching characteristics (VDD = 3.3V)

Input Characteristics ; VDD = +3.3V ± 0.3V, Temp. = 0～50 ℃

ITEM		SYMBOL	MIN.	MAX.	UNIT
CP Cycle	*1,*2	tCCL	100	―	ns
CP "H" Pulse Width	*2	tWCLH	40	―	ns
CP "L" Pulse Width	*2	tWCLL	40	―	ns
CP Rise Up Time	*2	trCP	―	30	ns
CP Fall Down Time	*2	tfCP	―	30	ns
Data Set Up Time		tDS	30	―	ns
Data Hold Time		tDH	20	―	ns
LOAD "H" Pulse Width		tWLPH	100	―	ns
LOAD "L" Pulse Width		tWLPL	4900	―	ns
LOAD Cycle	*3	tLCL	5000	―	ns
CP→LOAD Delay Time		tCL	0	―	ns
LOAD→CP Delay Time	*4	tLC	200-tWLPH	―	ns
Input Signal Rise Up Time		tr	―	30	ns
Input Signal Fall Down Time		tf	―	30	ns
FRM Data Set Up Time		tFS	100	―	ns
FRM Data Hold Time		tFH	30	―	ns

*1 CP Cycle is adjust so that FRM signal is 75Hz.

*2 The formula of condition
    ① trCP + tfCP ≦ tCCL - (tWCLH + tWCLL)
    ② trCP,tfCP ≦ 30 ns
  Please use on condition that ①,② are filled.

*3 LOAD Cycle is const.

*4 tLC ≧ 0

11-2. Switching characteristics (VDD = 5.0V)

Input Characteristics ; VDD = +5.0V $\pm$ 5%, Temp. = 0～50 ℃

ITEM	SYMBOL	MIN.	MAX.	UNIT
CP Cycle *1,*2	tCCL	62	—	ns
CP "H" Pulse Width *2	tWCLH	25	—	ns
CP "L" Pulse Width *2	tWCLL	25	—	ns
CP Rise Up Time *2	trCP	—	30	ns
CP Fall Down Time *2	tfCP	—	30	ns
Data Set Up Time	tDS	25	—	ns
Data Hold Time	tDH	20	—	ns
LOAD "H" Pulse Width	tWLPH	50	—	ns
LOAD "L" Pulse Width	tWLPL	370	—	ns
LOAD Cycle *3	tLCL	420	—	ns
CP→LOAD Delay Time	tCL	0	—	ns
LOAD→CP Delay Time *4	tLC	200-tWLPH	—	ns
Input Signal Rise Up Time	tr	—	30	ns
Input Signal Fall Down Time	tf	—	30	ns
FRM Data Set Up Time	tFS	100	—	ns
FRM Data Hold Time	tFH	30	—	ns

*1 CP Cycle is adjust so that FRM signal is 75Hz.

*2 The formula of condition
    ① trCP + tfCP $\leqq$ tCCL - (tWCLH + tWCLL)
    ② trCP, tfCP $\leqq$ 30 ns
  Please use on condition that ①,② are filled.

*3 LOAD Cycle is const.

*4 tLC $\geqq$ 0

１２．Supply Voltage Sequence Condition

Always follow the power supply ON / OFF sequence as specified below.
Unless you follow the power supply ON / OFF sequences, driving circuit in the LCD may irreparably break and / or DC voltage may be supplied to the LCD. DC voltage induces irreversible electrochemical reactions on the screen and reduce LCD life.
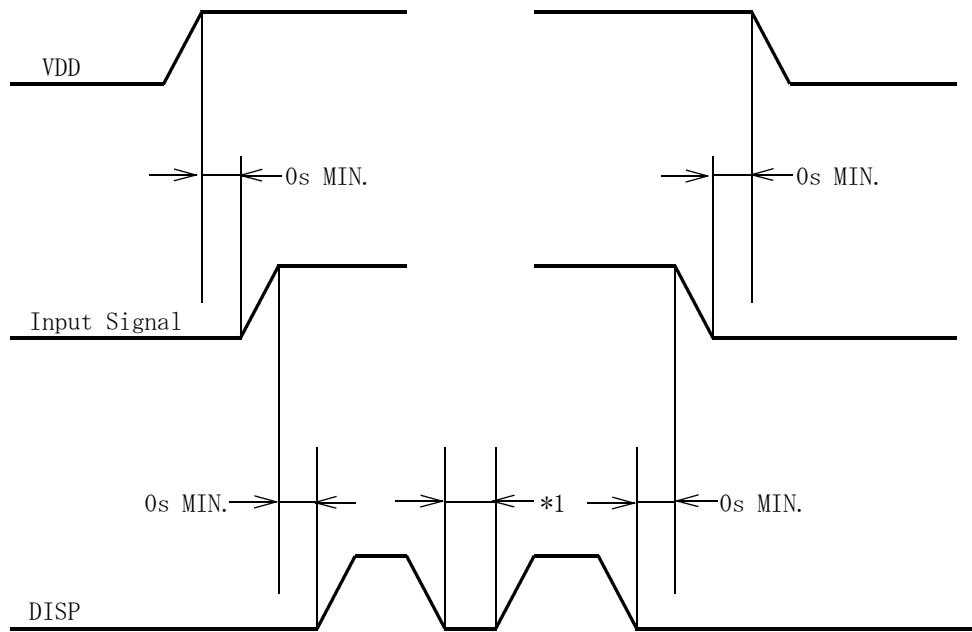
*1 DO NOT re-start driving the LCD within 500ms after once turning off DISP signal.

* Input signal : CP, LOAD, FRM, VCONT, HD0～HD7, LD0～LD7

* The above sequence should be designed as to keep each normal figure on condition that liquid crystal module is loaded on your system.

* Control the supply voltage sequence not to float all signal line when the LCD panel is driving.

13. Backlight Characteristics

CFL ratings

Temp.= 25℃

ITEM	SYMBOL	MIN.	TYP.	MAX.	NOTE
Starting discharge Voltage *1	VS	—	—	(935) Vrms.	0 ℃
		—	—	(650) Vrms.	25 ℃
Discharging tube current *2,*3	IL	(2.0)mArms.	(5.0)mArms.	(6.0)mArms.	—
Discharging tube voltage	VL	—	(385)Vrms.	—	—
Operating life *4 (IL=5.0 mArms.)	T	(25,000)h	(40,000)h	—	—
Operating frequency	F	(20) kHz	—	(100) kHz	—

*1 The Non-load output voltage (VS) of the inverter should be designed to have some margin, because VS may increase due to the leak current which may be caused by wiring of CFL cables.
(Reference value : ((1,216) Vrms MIN.)

*2 We recommend that you should set the discharging tube current at lower than typical value so as to prevent the heat accumulation of CFL tube from deteriorating a performance of the LCD.

*3 Do not apply more than 6.0mA discharging tube current.
Because CFL maybe broken due to over current.

*4 When the illuminance or quantity of light has decreased to 50% of the initial value, average life time of CFL will be decreased when LCD is operating at lower and higher temperature.

14. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.

ＫＨＳ０７２ＶＧ１ＡＢ－Ｇ０１ □　□－□　□

```
                              └──── DATE
                          └──────── MONTH
              └────────────────────── YEAR
```

YEAR	2004	2005	2006	2007	2008	2009
CODE	4	5	6	7	8	9

MONTH	JAN.	FEB.	MAR.	APR.	MAY	JUN.
CODE	1	2	3	4	5	6

MONTH	JUL.	AUG.	SEP.	OCT.	NOV.	DEC.
CODE	7	8	9	X	Y	Z

15. Warranty

15-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

15-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

16. Precautions for use

  16-1. Installation of the LCD

    1. Please ground either of the mounting (screw) holes located at each corner of an LCD module,
       in order to stabilize brightness and display quality.
    2. A transparent protection plate shall be added to protect the LCD and its polarizers.
    3. The LCD shall be installed so that there is no pressure on the LSI chips.
    4. The LCD shall be installed flat, without twisting or bending.
    5. The display window size should be the same as the effective viewing area.
    6. In case you use outside frame of effective viewing area as outward appearance of your
       product, unevenness of its outward appearance is out of guarantee.
    7. Do not pull the CFL lead wires and do not bend the root of the wires. Housing should be
       designed to protect CFL lead wires from external stress.
    8. When you attach or detach a connector, do not make any stress such as a warp and a twist
       to the LCD. Extremely strong stress may deteriorate a display quality like the LCD crack
       or line missing etc.
    9. This Kyocera LCD module has been specifically designed for use in general electronic
       devices, but not for use in a special environment such as usage in an active gas.
       Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD
       thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.

  16-2. Static Electricity

    1. Since CMOS ICs are mounted directly onto the LCD glass, protection from
       static electricity is required. Operation should wear ground straps.

  16-3. LCD Operation

    1. The LCD shall be operated within the limits specified. Operation at values outside of
       these limits may shorten life, and/or harm display images.
    2. Adjust "LCD driving voltage" to obtain optimum viewing angle and contrast.
    3. Operation of the LCD at temperature below the limit specified may cause image degradation
       and/or bubbles. It may also change the characteristics of the liquid crystal.
       This phenomenon may not recover. The LCD shall be operated within the temperature limits
       specified.

  16-4. Storage

    1. The LCD shall be stored within the temperature and humidity limits specified.
       Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
    2. Always store the LCD so that it is free from external pressure onto it.

  16-5. Screen Surface

    1. DO NOT store in a high humidity environment for extended periods. Image degradation,
       bubbles, and/or peeling off of polarizer may result.
    2. The front polarizer is easily scratched or damaged. Prevent touching it with
       any hard material, and from being pushed or rubbed.
    3. The LCD screen may be cleaned with a soft cloth or cotton pad. Methanol, or
       Isopropyl Alcohol may be used, but insure that all solvent residue is removed.
    4. Water may cause damage or discoloration of the polarizer. Clean any condensation or
       moisture from any source immediately.
    5. Always keep the LCD free from condensation during testing.
       Condensation may permanently spot or stain the polarizers.

17. Reliability Data / Environmental Test

TEST ITEM	TEST CONDITION	TEST TIME	RESULT
High Temp. Atmosphere	60℃	240 h	Display Quality : No defect Display Function : No defect Current Consumption : No defect
Low Temp. Atmosphere	−20℃	240 h	Low Temp. Bubble : None Solid Crystallization of Liquid Crystal : None Display Quality : No defect Display Function : No defect Current Consumption : No defect
High Temp. Humidity Atmosphere	40℃ 90%RH	240 h	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Current Consumption : No defect
Temp. Cycle	−20℃ 0.5 h R.T. 0.5 h 60℃ 0.5 h	10 cycles	Display Quality : No defect Display Function : No defect Peel-off of Organic Sealing : None Bubble on Cell : None
High Temp. Operation	50℃ Vop	500 h	Display Quality : No defect Current Consumption : No defect

* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

* The LCD is tested in circumstances in which there is no condensation.

* The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.

* The reliability test is not an out-going inspection.

* The result of the reliability test are for your reference purpose only.
  The reliability test is conducted only to examine the LCD's capability.

DETAIL A (NTS)
(Dot Size)

P0.228
0.208 (0.02)
P0.076
0.056

P0.228
0.208
(0.02)

R G B R G B
R G B R G B

insert direction

DETAIL B

Electric Conducting Tape
(W4xL15±3)

179.3
172.8±0.3
147.9(Viewing Area)
145.9(Active Area)
(92.08)
(80.72)
18.13
1
4

50±10
2.5
(6.77)
(1)

CN2

No.3 (White)
No.1 (Pink)

5.05
9.55

130.5
120.4±0.3 (Viewing Area)
111.4 (Viewing Area)
109.42 (Active Area)
(60.2)
(60.2)
(60.2)

CN 1 No.31
No.01 No.30
No.02

640×(R,G,B)×480 dots

19.7
15.2
81
90±0.3

(24.74)
(20.24)

(5.05)
(9.55)
(1)

DETAIL C

Electric Conducting Tape
(W4xL15±3)

DETAIL D

6

2- R15
2-3±0.2
9
2.5±0.2
3.45max
0.8
3.5±0.2
5.5±0.2
6

DETAIL D (2:1)

M2.6 Screw
2-φ3±0.2
2-φ7±0.2
4
0.8
3.5±0.2
5.5±0.2
6
4

DETAIL C (2:1)

CN1
PCB
5±0.5
1±0.5

DETAIL B (2:1)

Note

1 Connector CN1: DF9B-31P-1V(HIROSE)
   (コネクター)   CN2: BHR-03VS-1(JST)

2 Matching Connector : DF9B-31S-1V(HIROSE)
   (適合コネクター)   : SM02-(8.0)B-BHS-1(JST)

3 The drawing is the front view of the LCD. Information
   is displayed starting at the upper left hand corner, moving
   right then down to the lower right hand corner.
   (LCDの正面図において、画像データの表示は左上コーナーから始まり、
    右へ進み下へ送られ右下コーナーへ向かう。)

4 Tolerance without indication; ±0.5
   (指示無き公差)

No	Description	Drawn	Checked	Checked	Approved

| Material 材質 | Treatment 処理 | | Approved '03.10.14 朝倉 | Checked | Checked '03.10.14 今村 | Drawn 堀 | Scale 1:1 (2:1,NTS) | Title | KHS072VG1AB | KYOCERA | Year–Month–Day '03.10.13 | Size 2 |
| Quantity 製作数 | Description; 備考 03004NDA | | | | | | | | Outline Dimensions | | Drawing No. 121A5025600 | |

SPEC. No.	TQ3C-8EAC0-E2CWJ29-00
DATE	April 26, 2004

FOR : _____

KYOCERA INSPECTION STANDARD

TYPE : KHS072VG1AB-G01

KYOCERA CORPORATION
KAGOSHIMA HAYATO PLANT
LCD DIVISION

Original	Designed by : Engineering Dept.			Confirmed by : QA Dept.	
Issue Data	Prepared	Checked	Approved	Checked	Approved
April 26, 2004	Y. Yamazaki	K. Yangu	H. Ohno	Y. Yoshida	S. Hayashi

Revision Record
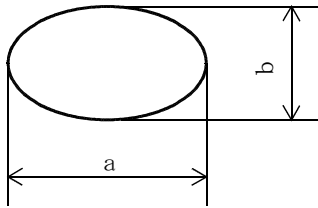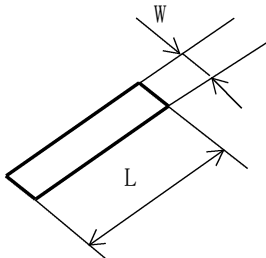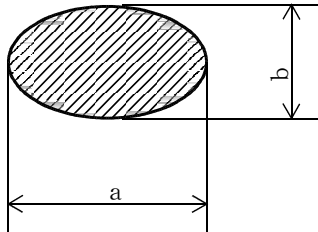
Date	Designed by: Engineering Dept.			Confirmed by: QA Dept.	
	Prepared	Checked	Approved	Checked	Approved

Rev. No.	Date	Page	Descriptions

Designed by: Engineering Dept.   Confirmed by: QA Dept.

Visuals specification

1)Note

Item	Note	
General	1.When defects specified in this Inspection Standards are inspected, operating voltage(Vop) shall be set at the level where optimized contrast is available. Display quality is applied up to effective viewing area. (Bi-Level INSPECTION) 2.This inspection standard about the image quality shall be applied to any defect within the effective viewing area and shall not be applicable to outside of the area. 3.Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and Kyocera. 4.Inspection conditions Luminance : 500 Lux minimum . Inspection distance : 300 mm (from the sample) Temperature : 25 ± 5 ℃ Direction : right above	
Definition of Inspection item	Pinhole, Bright spot Black spot, Scratch Foreign particle	The color of a small area is different from the remainder. The phenomenon does not change with voltage.
	Contrast variation	The color of a small area is different from the remainder. The phenomenon changes with voltage.
	Polarizer (Scratch, Bubble, Dent)	Scratch, Bubble and Dent in the polarizer which can be observed in on / off state.

2) Standard

Inspection item	Judgement standard
Pinhole, Bright spot Black spot, Foreign particle	$d = (a + b) / 2$ 	Category	Size (mm)	Acceptable number
---	---	---
A	$d \leqq 0.2$	neglected
B	$0.2 < d \leqq 0.3$	5
C	$0.3 < d \leqq 0.5$	3
D	$0.5 < d$	0	
Scratch, Foreign particle			Width (mm)	Length (mm)	Acceptable No.
---	---	---	---
A	$W \leqq 0.03$	—	neglected
B		$L \leqq 2.0$	neglected
C	$0.03 < W \leqq 0.1$	$2.0 < L \leqq 4.0$	3
D		$4.0 < L$	0
E	$0.1 < W$	—	According to 'Circular'	
Contrast variation	$d = (a + b) / 2$ 	Category	Size (mm)	Acceptable number
---	---	---
A	$d \leqq 0.5$	neglected
B	$0.5 < d \leqq 0.7$	3
C	$0.7 < d$	0	

Inspection item	Judgement standard
Polarizer (Scratch, Bubble, Dent)	(1) Scratch

	Width (mm)	Length (mm)	Acceptable No.
A	W ≦ 0.1	—	neglected
B	0.1 < W ≦ 0.3	L ≦ 5.0	neglected
C		5.0 < L	0
D	0.3 < W	—	0

(2)Bubble (dent)

$$d = (a + b) / 2$$

Category	Size (mm)	Acceptable number
A	d ≦ 0.2	neglected
B	0.2 < d ≦ 0.3	5
C	0.3 < d ≦ 0.5	3
D	0.5 < d	0