These drawings and specification			REVISIONS		
the property of Densitron Corpor and may not be reproduced, cop	ied or R	EV	DESCRIPTION	DATE	APPROVED
used without written permission	on	A	E0147		
features. 3. All dimensions are in m 4. Precautions:These prec	cifications app illimeters. autions apply e	ly to equ	otice. Standard modules. This information may vary ally to modules from all makers, not just Densit cause problems ranging from erratic operation	ron. Violation of	these
Handling precautions ♦ This device is su		ctro	-Static Discharge (ESD) damage. Observe Anti-Sta	atic precautions.	
 variance betweet Prevent the app Use a clean power maximum rating The +5V power the data bus to be data bus	all times, observe in models. lication of revers ver source free fi s of the module sho of the module sho driven when t a capacitor betw citor combines w damaging the mod hs:	noul noul he l veen vith odul	solute maximum ratings for both logic and LC drive plarity to VDD and Vss, however briefly. transients. Power up conditions are occasionally ' d also supply the power to all devices which may a ogic supply to the module is turned off. the Vo (contrast) pin and ground. VDD must, at all the contrast potentiometer to form an R-C network e. when the system is powered up. he module and host MPU. (Recommended max. le	'jolting" and may e ccess the display. I times, exceed th which "holds-up"	exceed the . Don't allow e Vo voltage
 For models with voltage extreme 	EL backlights, c s which may arc	do no viti	of the modules temperature specifications.		rs produce
under the elasto solder.	ng is the major of meric connectio	caus n ar	se of module difficulty. Use of flux cleaner is not re ad cause display failure. Densitron recommends th		
 Surface of LCD polarizer. Avoid benzene. 	panel should no contact and cle	t be an c	om torque and mechanical stress. touched or scratched. The display front surface is only when necessary with soft, absorbent cotton da		
	e build-up upon	the	are while handling the module. module and observe the environmental constraints	for storage tempe	erature and
 If leakage of the 	liquid crystal ma	ateri	al should occur, avoid contact with this material, pa by the liquid crystal material, wash thoroughly with v		n. If the body
Notes: (unless otherwis	e specified)				
Unless otherwise APPROVA specified:	LS DATE		DENSITRON COR	PORATI	
DRAWN Dimensions are mm			TORRANCE,		

Dimensions are mm		1		
Tolerances are: $X = \pm 3$ $X = \pm 0.5$	CHECKED	TITLE	2 LINE X 16 CHARACTERS LCD M	ODULE
.XX = ± 0.05	ISSUED	DWG. NO.	LM4304	SHEET 1 OF 8

1.0 **DESCRIPTION**

Dot matrix display module consisting of a Liquid Crystal Display, CMOS driver and controller LSI, printed circuit board, metal support frame and array type Light Emitting Diode (LED) backlight.

Available LC fluids types are: NTN (supertwisted nematic), NTN-H (extended temperature range NTN).

Options include on-board negative voltage generation.

2.0 MECHANICAL CHARACTERISTICS

Item	Specifications	Unit
Package Dimensions	151.0 (W) x 40.0 (H) x 17.5 max. (D)	mm
Display format	2 line x 16 characters	-
Character font format	5 (W) x 7 (H) with attached cursor	dots
Driving method	1/16	duty
Dot size	1.16 (W) x 1.16 (H)	mm
Dot pitch	1.21 (W) x 1.21 (H)	mm
Character Size	6.0 (W) x 9.63 (H)	mm
Active display area	114.0 (W) x 20.64 (H)	mm
Viewing area	120.0 (W) x 23.0 (H)	mm
Weight		g

Notes:W-Width;H-Height;D-Depth.

3.0 ABSOLUTE MAXIMUM RATINGS

VSS=0V;Ta=25°C

Item	Symbol	nbol <u>TN, NTN</u> TI		TN-H,	NTN-H	Unit
		Min.	Max.	Min.	Max.	
Logic supply voltage	VDD-VSS	0	7	0	7	V
LC driver supply voltage	Vdd-Vo	0	6	0	13	V
Operating temperature	Тор	0	+50	-20	+70 (Note 3)	С°
Storage temperature (Note 1)	Ts⊤	-20	+70	-30	+80	
Humidity: Operating (@40°C)	-	-	85%	-	85%	RH (Note 2)
Non-operating (@40°C)	-	-	95%	-	95%	RH (Note 2)

Notes: 1: Tested to 100 hrs.

2: Refers to non-condensing conditions.

3. With backlight off.

4.0 ELECTRICAL CHARACTERISTICS

					Vdd=5±0.2	5V;Ta=25°C
Item	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Input "High" voltage	Vih	-	2.2	-	Vdd	V
Input "Low" voltage	VIL	-	-	-	0.6	V
Output "High" voltage	Vон	Іон=0.205mA	2.4	-	-	V
Output "Low" voltage	Vol	IoL=1.2mA	-	-	0.4	V
Power supply current	ldd	Vdd=5.0V	-	1	-	mA

DWG. NO. LM4304	SHEET 2 OF 8	А

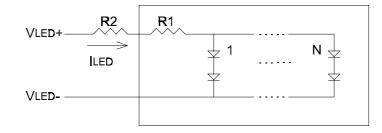
RECOMMENDED LC DRIVE VOLTAGE (VDD-VO) 5.0

		۰.		Vdd=5.0±0.25V
Temperature	TN	TN-H	NTN	NTN-H
Ta= -20°C	-	-	-	5.8
Ta= 0°C	-	-	5.0	5.3
Ta= 25°C	-	-	4.8	5.0
Ta= 50°C	-	-	4.6	4.8
Ta=70°C	-	-	-	4.6

6.0 **BACKLIGHT SPECIFICATIONS:**

Ta=20°C,60%RH,Darkroom.

Item	Symbol	Тур.	Max.	Unit
LED input voltage	VLED	5	6	V
LED input current	ILED	460	500	mA
Built-in current limiting resistor	R1	-	-	Ohms, W
External current limiting resistor (recommended)	R2	2 Ohm, 1 W	-	Ohms, W
Number of nodes	N	46	-	-

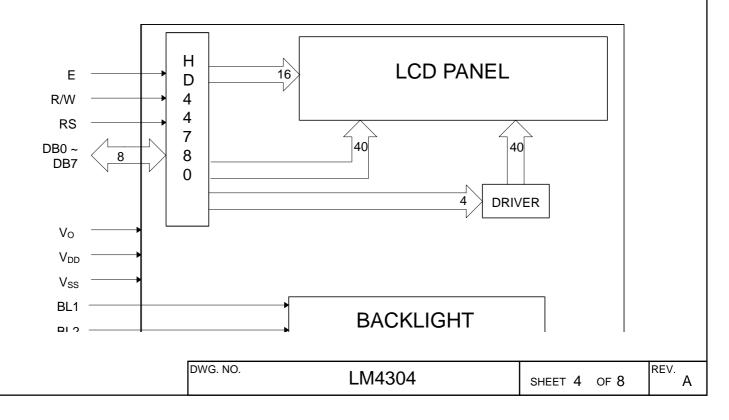


7.0 POWER SUPPLY TN-H, NTN-H 7.0

TN, NTN V dd Vdd +5V Vo Vκ Vo Vr L C D L C D +5V -5V Vss Vss $\overline{}$ \square VR= 10K - 20K ohm DWG. NO. REV. LM4304 А 3 OF 8 SHEET

8.0 INTERFACE DESCRIPTION

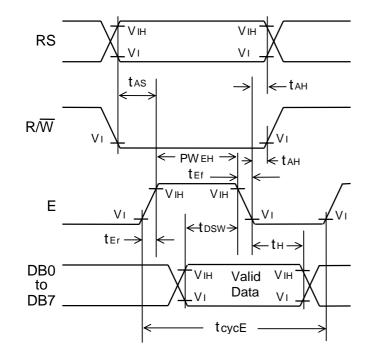
Pin No.	Symbol	I/O	Function
1	Vss	-	Ground (0V)
2	Vdd	-	Logic Supply Voltage (+5V)
3	Vo	-	LC Drive voltage for contrast adjustment
4	RS	I	Register Select 0: Instruction Register
			1: Data Register
5	R/W	I	Read / Write 0: Data Write (Module ← MPU)
			1: Data Read (Module→MPU)
6	E		Enable Signal Active High (H→L)
7	DB0	I/O	Bi-directional data bus line 0
8	DB1	I/O	Bi-directional data bus line 1
9	DB2	I/O	Bi-directional data bus line 2
10	DB3	I/O	Bi-directional data bus line 3
11	DB4	I/O	Bi-directional data bus line 4
12	DB5	I/O	Bi-directional data bus line 5
13	DB6	I/O	Bi-directional data bus line 6
14	DB7	I/O	Bi-directional data bus line 7
15	N/C (VEE)	- (O)	No connection (Negative voltage output for models with on-
			board negative voltage generator)
16	N/C	-	No connection
BL1	Vled+	-	Supply voltage for backlight (+5V)
BL2	Vled-	-	Supply voltage for backlight (0V)



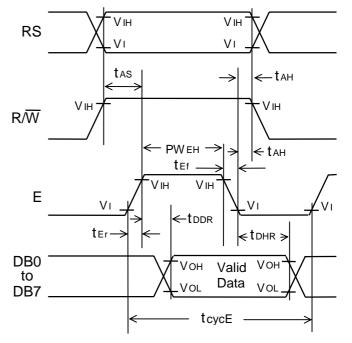
10.0 TIMING CHARACTERISTICS

Item	Symbol	Min.	Тур.	Max.	Unit
Enable cycle time	TcycE	500	-	-	nS
Enable pulse width	РWeh	230	-	-	nS
Enable rise / fall time	tEr/tEf	-	-	20	nS
Address set-up time	tas	40	-	-	nS
Address hold time	tан	10	-	-	nS
Data delay time	tddr	-	-	160	nS
Data hold time (Write)	t DHW	10	-	-	nS
Data hold time (Read)	t DHR	5	-	-	nS
Data set-up time	tDSW	80	-	-	nS

WRITE OPERATION



READ OPERATION



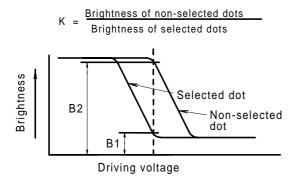
11.0 DD RAM ADDRESS vs. DISPLAY POSITION

Character	1	2	3	4	5	6	7	8	9	10	11		14	15	16
Line 1	00	01	02	03	04	05	06	07	08	09	0A		0D	0E	0F
Line 2	40	41	42	43	44	45	46	47	48	49	4A		4D	4E	4F
				DWG.	NO.										REV.
								L	M430)4		SHEET	5	of 8	

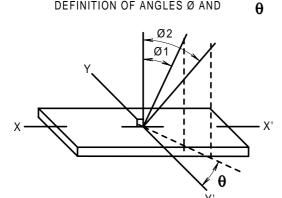
12.0 OPTICAL CHARACTERISTICS

lte	em	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Contrast ratio TN	N, TN-H	K	Ø=20° θ=0°	3	-	-	-
Contrast ratio N	ΓN	K	Ø=20° θ=0°	4	-	-	-
Contrast ratio N	ГN-H	K	Ø=20° θ=0°	5	-	-	-
Viewing angle	TN, TN-H	Ø2-Ø1	θ=0° K <u>></u> 1.4	20	-	-	Deg.
		θ	Ø=20° K=1.4	±30	-	-	Deg.
Viewing angle	NTN	Ø2-Ø1	θ=0° K <u>></u> 1.4	40	-	-	Deg.
		θ	Ø=20° K=1.4	±30	-	-	Deg.
Viewing angle	NTN-H	Ø2-Ø1	θ=0° K <u>></u> 1.4	40	-	-	Deg.
		θ	Ø=20° K=1.4	±40	-	-	Deg.
Response time	Rise	tr	Ø=20° θ=0°	-	150	250	mS
	Fall	tf	Ø=20° θ=0°	-	150	250	mS

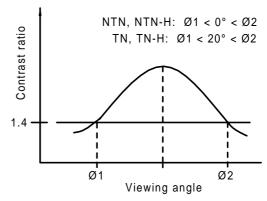




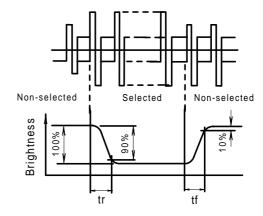
DEFINITION OF ANGLES Ø AND



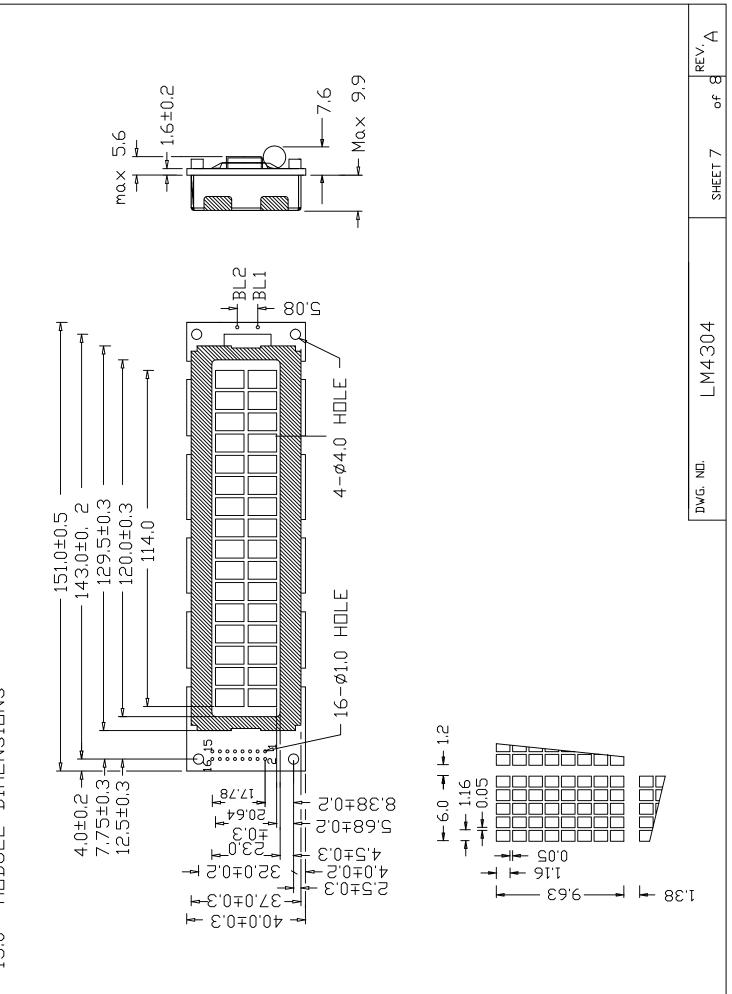
CONTRAST VERSUS VIEWING ANGLE



DEFINITION OF OPTICAL RESPONSE



DWG. NO. REV. LM4304 SHEET 6 OF 8 А



13.0 MODULE DIMENSIONS

14.0 PART NUMBER DESCRIPTION FOR AVAILABLE OPTIONS		
	LM4304①②2C16③④⑤	
OPTION 5.DOC		
	Polarizer Type B = Transflective: light background with LED backlight E = Transmissive: dark background with LED backlight F = Transmissive: light background with LED backlight	
2	Backlight Color G = Yellow-green (standard) R = Red	
3	Fluid Type and Power Supply S = NTN with +5VDC operation H = NTN-H with ±5VDC operation W = NTN-H with +5VDC operation (on-board negative voltage generation)	
4	Fluid Type N = NTN, NTN-H	
(5)	Background Color for NTN Fluid B = Blue background G = Gray background Y = Yellow background	

DWG. NO. LM4304 SHEET 8 OF 8	^{/.} А
------------------------------	-----------------