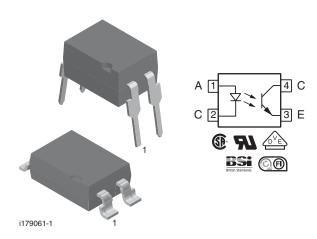


Optocoupler, Phototransistor Output, Low Input Current



DESCRIPTION

The SFH618A (DIP) and SFH6186 (SMD) feature a high current transfer ratio, low coupling capacitance and high isolation voltage. These couplers have a GaAs infrared diode emitter, which is optically coupled to silicon planar phototransistor detector, and is incorporated in a plastic DIP-4 or SMD package.

The coupling devices are designed for signal transmission between two electrically separated circuits. The couplers are end-stackable with 2.54 mm lead spacing. Creepage and clearance distances of > 8 mm achieved with option 6. This version complies with IEC 60950 (DIN VDE 0805) for reinforced insulation to an operation voltage of 400 V_{RMS} or DC.

FEATURES

 Good CTR linearity depending on forward current



- Low CTR degradation
- High collector emitter voltage, V_{CEO} = 55 V
- Isolation test voltage, 5300 V_{RMS}
- Low coupling capacitance
- End stackable, 0.100" (2.54 mm) spacing
- High common mode transient immunity
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

APPLICATIONS

- Telecom
- · Industrial controls
- Battery powered equipment
- Office machines

AGENCY APPROVALS

- UL1577, file no. E52744 system code H or J, double protection
- CSA 93751
- DIN EN 60747-5-2 (VDE 0884) available with option 1
- BSI IEC60950; IEC60065
- FIMKO

ORDERING INFORMATION	N			
S F H 6 1 PART NUMBE		# X 0 # CTR PACKAGE OP	TION TAPE	7.62 mm
AGENCY CERTIFIED/PACKAGE	·	CTF	२ (%)	·
AGENOT GENTILIED/T AGRAGE		11	mA	
UL, CUL	63 to 125	100 to 200	160 to 320	250 to 500

AGENCY CERTIFIED/PACKAGE	O111 (70)						
AGENCT CENTIFIED/FACKAGE		1 r	1 mA				
UL, CUL	63 to 125 100 t		160 to 320	250 to 500			
DIP-4	SFH618A-2	SFH618A-3	SFH618A-4	SFH618A-5			
DIP-4, 400 mil, option 6	=	SFH618A-3X006	=	=			
SMD-4, option 7	=	-	=	SFH618A-5X007T (1)			
SMD-4, option 9	SFH6186-2T ⁽¹⁾	SFH6186-3T ⁽¹⁾ , SFH6186-3X002T ⁽¹⁾	SFH6186-4T ⁽¹⁾	SFH6186-5T ⁽¹⁾ , SFH6186-5T1 ⁽²⁾			
VDE, UL, CUL	63 to 125	100 to 200	160 to 320	250 to 500			
DIP-4	-	SFH618A-3X001	SFH618A-4X001	-			
DIP-4, 400 mil, option 6	=	SFH618A-3X016	SFH618A-4X016	SFH618A-5X016			
SMD-4, option 7	=	SFH618A-3X017	=	SFH618A-5X017 (1)			
SMD-4, option 9	-	SFH618A-3X001T (1)	SFH618A-4X001T	SFH618A-5X001T (1)			

Notes

Rev. 2.0, 12-Aug-11

- · Additional options may be possible, please contact sales office
- (1) Also available in tubes, do not put T to the end
- (2) Product is rotated 180° in tape and reel cavity



PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V _R	6	V
Power dissipation		P _{diss}		mW
Forward current		I _F	60	mA
OUTPUT				
Collector emitter voltage		V _{CE}	55	V
Emitter collector voltage		V _{EC}	7	V
O. H. alamana		I _C	50	mA
Collector current	t _p ≤ 1 ms	I _C	100	mA
Power dissipation		P _{diss}	150	mW
COUPLER				
Isolation test voltage between emitter and detector		V _{ISO}	5300	V _{RMS}
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω
isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω
Storage temperature range		T _{stg}	- 55 to + 150	°C
Ambient temperature range		T _{amb}	- 55 to + 100	°C
Junction temperature		Tj	100	°C
Soldering temperature (1)	max. 10 s, dip soldering distance to seating plane ≥ 1.5 mm	T _{sld}	260	°C

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

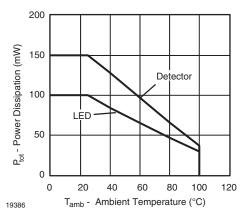


Fig. 1 - Permissible Power Dissipation vs. Ambient Temperature



ELECTRICAL CHARACTE	ERISTICS (T _{amb} = 25 °C	C, unless ot	herwise sp	pecified)			
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT							
Forward voltage	$I_F = 5 \text{ mA}$		V_{F}		1.1	1.5	V
Reverse current	V _R = 6 V		I _R		0.01	10	μΑ
Capacitance	$V_R = 0 V, f = 1 MHz$		Co		25		pF
Thermal resistance			R _{thja}		1070		K/W
OUTPUT							
Collector emitter leakage current	V _{CE} = 10 V		I _{CEO}		10	200	nA
Collector emitter capacitance	V _{CE} = 5 V, f = 1 MHz		C _{CE}		7		pF
Thermal resistance			R _{thja}		500		K/W
COUPLER							
	$I_C = 0.32 \text{ mA}, I_F = 1 \text{ mA}$	SFH618A-2	V_{CEsat}		0.25	0.4	V
	IC = 0.32 IIIA, IF = 1 IIIA	SFH6186-2	V_{CEsat}		0.25	0.4	V
	05 mΛ 1 mΛ	SFH618A-3	V _{CEsat}		0.25	0.4	V
Collector emitter saturation	$I_C = 0.5 \text{ mA}, I_F = 1 \text{ mA}$	SFH6186-3	V _{CEsat}		0.25	0.4	V
voltage	-0.9 m	SFH618A-4	V_{CEsat}		0.25	0.4	V
	I _C = 0.8 mA, I _F = 1 mA	SFH6186-4	V _{CEsat}		0.25	0.4	V
		SFH618A-5	V _{CEsat}		0.25	0.4	V
	$I_C = 1.25 \text{ mA}, I_F = 1 \text{ mA}$	SFH6186-5	V _{CEsat}		0.25	0.4	V
Coupling capacitance			C _C		0.25		pF

Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
		SFH618A-2	CTR	63		125	%
	$I_F = 1 \text{ mA}, V_{CE} = 0.5 \text{ V}$	SFH6186-2	CTR	63		125	%
	1 05 1 5 1	SFH618A-2	CTR	32	75		%
	$I_F = 0.5 \text{ mA}, V_{CE} = 1.5 \text{ V}$	SFH6186-2	CTR	32	75		%
	1 1 m A V 0 5 V	SFH618A-3	CTR	100		200	%
	$I_F = 1 \text{ mA}, V_{CE} = 0.5 \text{ V}$	SFH6186-3	CTR	100		200	%
	1 0 5 m A V 1 5 V	SFH618A-3	CTR	50	120		%
	$I_F = 0.5 \text{ mA}, V_{CE} = 1.5 \text{ V}$	SFH6186-3	CTR	50	120		%
I _C /I _F	1 1 77 1 1 77 1 1 1 1 1 1 1 1 1 1 1 1 1	SFH618A-4	CTR	160		320	%
	$I_F = 1 \text{ mA}, V_{CE} = 0.5 \text{ V}$	SFH6186-4	CTR	160		320	%
	1 05 1 5 1	SFH618A-4	CTR	80	200		%
	$I_F = 0.5 \text{ mA}, V_{CE} = 1.5 \text{ V}$	SFH6186-4	CTR	80	200		%
	1 1 77 1 1 77 1 1 1 1 1 1 1 1 1 1 1 1 1	SFH618A-5	CTR	250		500	%
	$I_F = 1 \text{ mA}, V_{CE} = 0.5 \text{ V}$	SFH6186-5	CTR	250		500	%
	1 0 5 m A V 1 5 V	SFH618A-5	CTR	125	300		%
	$I_F = 0.5 \text{ mA}, V_{CE} = 1.5 \text{ V}$	SFH6186-5	CTR	125	300		%

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Turn on time	V_{CC} = 5 V, I_C = 2 mA, R_L = 100 Ω	t _{on}		6		μs	
Rise time	V_{CC} = 5 V, I_C = 2 mA, R_L = 100 Ω	t _r		3.5		μs	
Turn off time	$V_{CC} = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$	t _{off}		5.5		μs	
Fall time	V_{CC} = 5 V, I_C = 2 mA, R_L = 100 Ω	t _f		5		μs	



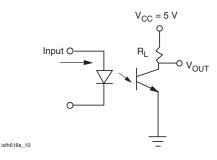


Fig. 2 - Test Circuit

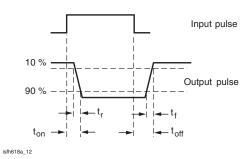


Fig. 3 - Test Circuit and Waveforms

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC68 part 1)				55/100/21		
Comparative tracking index		CTI	175		399	
V _{IOTM}			10000			V
V _{IORM}			890			V
P _{SO}					400	mW
I _{SI}					275	mA
T _{SI}					175	°C
Creepage distance	Standard DIP-4		7			mm
Clearance distance	Standard DIP-4		7			mm
Creepage distance	400 mil DIP-4		8			mm
Clearance distance	400 mil DIP-4		8			mm
Insulation thickness, reinforced rated	per IEC60950 2.10.5.1		0.4			mm

Note

As per IEC60747-5-2, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

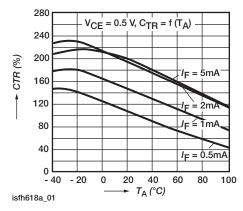


Fig. 4 - Current Transfer Ratio (typ.)

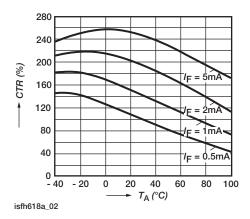


Fig. 5 - Current Transfer Ratio (typ.)



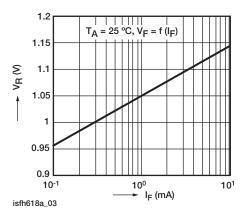


Fig. 6 - Diode Forward Voltage (typ.)

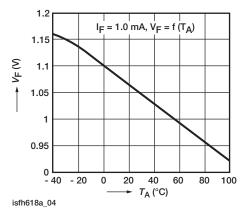


Fig. 7 - Diode Forward Voltage (typ.)

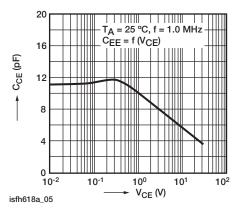


Fig. 8 - Transistor Capacitance

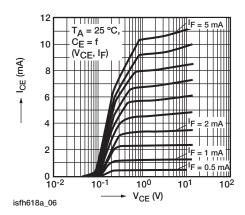


Fig. 9 - Output Characteristics

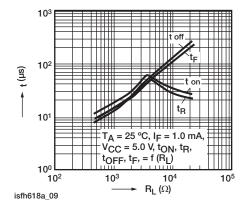
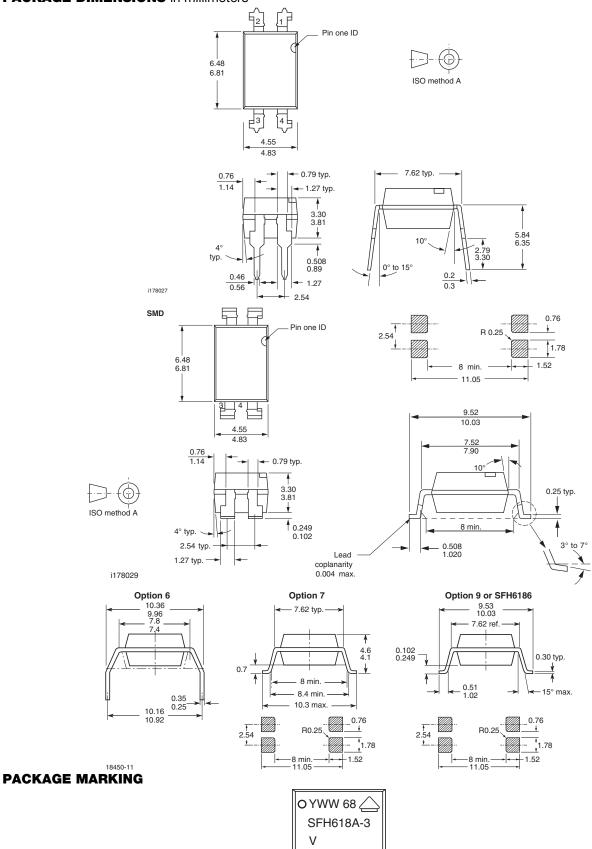


Fig. 10 - Switching Times (typ.)



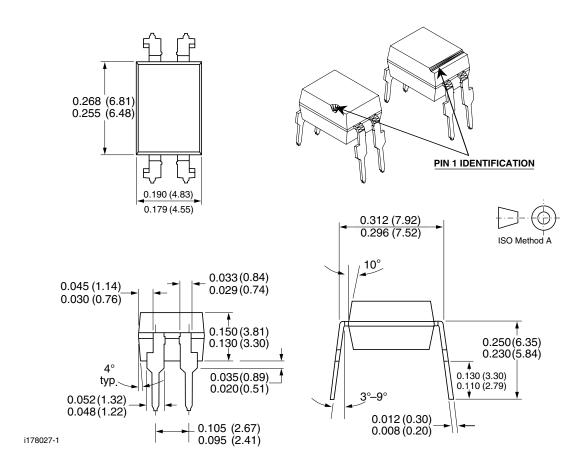
PACKAGE DIMENSIONS in millimeters







Package Dimensions in Inches (mm)



TH-DIP-4

Vishay Semiconductors



Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

> We reserve the right to make changes to improve technical design and may do so without further notice.

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Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany

www.vishay.com Rev. 1.2, 29-Aug-06





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