1.4A HIGH-SPEED OPTO-ISOLATED POWER MOSFET DRIVER

FEATURES

- **Input to Output Isolation to 2.5kV RMS**
- **Operating Range ..... 10V to 18V**
- **High Peak Output Current ..... 1.4A Typ**
- **Short Delay Time ..... <250 ns Typ**
- **Fast Switching on Outputs ..... T_R, T_F < 80ns Typ ..... with C_L = 1000pF**
- **Low Power BiCMOS Design**
- **Undervoltage Lock-out with Hysteresis**
- **Available in 8-pin DIP with SMT-formed leads**

APPLICATIONS

- **Isolated Digital Line Driver**
- **Isolated Line Receiver**
- **"High-Side" Driver**
- **SMPS Control**
- **High-side Motor Control**
- **Solid State Relays**
- **Off-Line Regulation/Control**

UL File No: E151672 on TC4803/4

GENERAL DESCRIPTION

The TC4803 AND TC4804 are BiCMOS optocoupled driver ICs for switching loads when electrical isolation is desired. Input drive current is converted to low impedance voltage drive with the ability to source 1.4A peak current into a capacitive load of 1,000 pF with fast output rise and fall times. UV lockout circuitry forces the output to an "OFF" state when the supply voltage drops below 7.8V. 0.4V of hysteresis prevents output toggling around the drop-out voltage. The output "OFF" state is LOW on both the TC4803 and the TC4804.

Because shoot-through currents are reduced in the output stage, these drivers consume significantly less power at higher frequencies, and can be helpful in meeting low-power budgets.

These devices are built using TelCom Semiconductor's new Tough CMOS process and are capable of giving reliable service in the most demanding electrical environments. They will not latch under normal conditions within their power and voltage ratings.

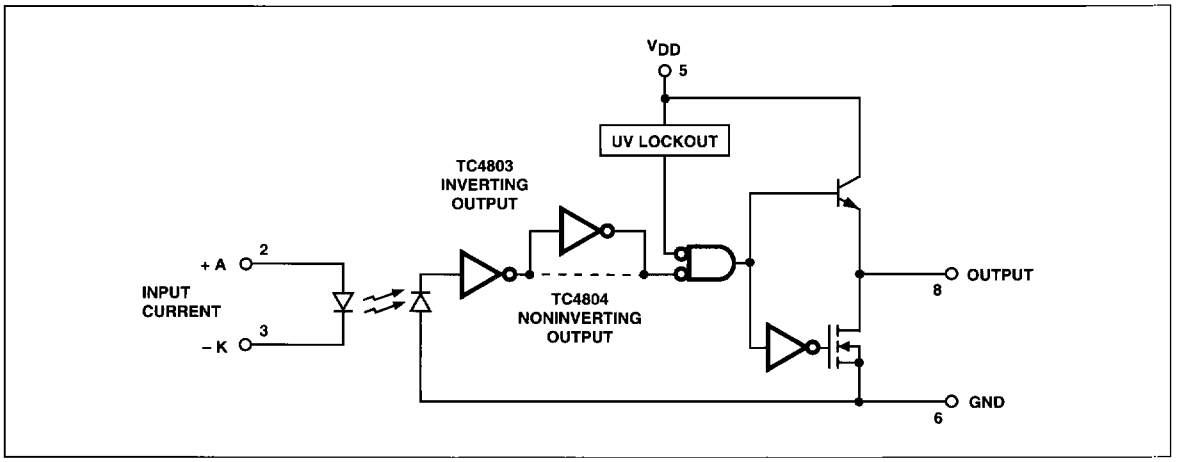
All terminals are fully protected against up to 4 kV of electrostatic discharge.

ORDERING INFORMATION

Part No.	Package	Operating Temp Range
TC4803CPA	8-Pin PDIP	0°C to +70°C
TC4803EPA	8-Pin PDIP	–40°C to +85°C
TC4803EFA*	8-Pin PDIP*	–40°C to +85°C
TC4803CFA*	8-Pin PDIP*	0°C to +70°C
TC4804CPA	8-Pin PDIP	0°C to +70°C
TC4804EPA	8-Pin PDIP	–40°C to +85°C
TC4804EFA*	8-Pin PDIP*	–40°C to +85°C
TC4804CFA*	8-Pin PDIP*	0°C to +70°C

* Plastic DIP with preformed surface mount leads

FUNCTIONAL BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Supply Voltage ..... +20V
Diode Current Input 20mA
Maximum Chip Temperature ..... 85°C
Storage Temperature Range ..... –55°C to +125°C
Lead Temperature (Soldering, 10 sec) ..... +300°C

Package Thermal Resistance
PDIP $R_{\theta J-A}$ ..... 200°C/W
Operating Temperature Range
Thermal Derating ..... 5mW/°C above 25°C
Power Dissipation 300mW

Static-sensitive device. Unused devices must be stored in conductive material. Protect devices from static discharge and static fields. Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to Absolute Maximum Rating Conditions for extended periods may affect device reliability.

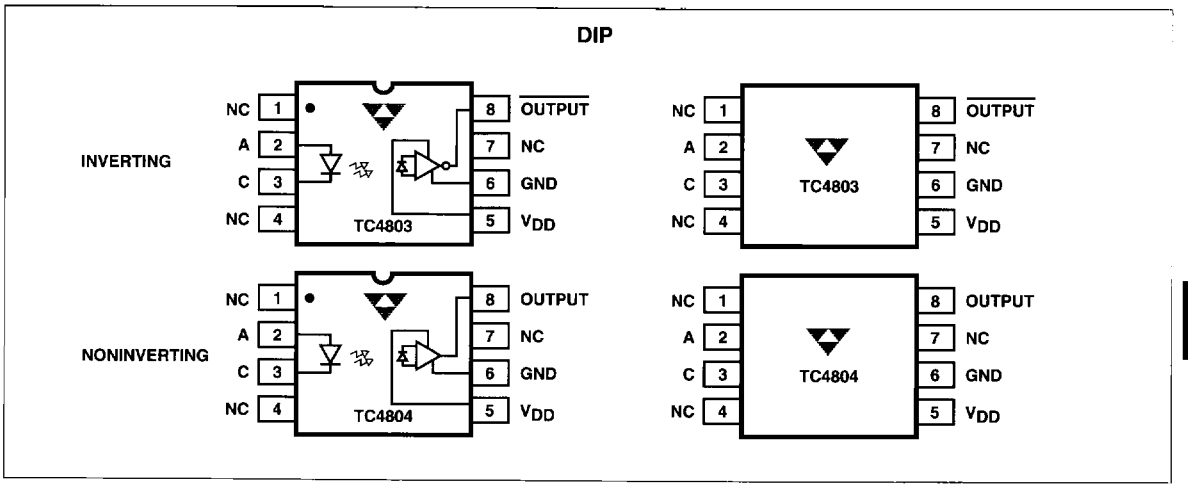
ELECTRICAL CHARACTERISTICS:

Typical specifications measured at T_A = +25°C with V_{DD} = 18V, unless otherwise specified. Minimum and maximum specifications guaranteed over full temperature and power supply range.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Input LED						
$I_{F(TH)}$-ON	LED Forward Threshold "ON"-Current (DC)		5	—	—	mA
$I_{F(TH)}$-OFF	LED Forward Threshold "OFF"-Current (DC)		—	—	0.5	mA
V_F	LED Forward Voltage @ 7 mA Current		1.3	1.5	1.7	V
V_R	LED Reverse Voltage @ 50nA		6.0	—	—	V
	Isolation Voltage		2.5	—	—	kVRMS
Output						
V_{OH}	High Output Voltage, V_{DD} =18V, I_{OUT} = 50mA		16.3	16.9	—	V
V_{OL}	Low Output Voltage, V_{DD} =18V, I_{OUT} = 50mA		—	0.70	1.0	V
I_{PK}	Peak Output Current (Note 1)	Source	—	1.4	—	A
		Sink		0.5		A
I_{DC}	Continuous Output Current V_{DD} = 18V (Source, sink)		100	—	—	mA
	DV/DT Input to Output Common Mode Transient Immunity @ 150V		0.25	—	—	V/ns
Switching Time						
t_R	Rise Time	Figure 1	—	35	60	ns
t_F	Fall Time	Figure 1	—	40	80	ns
t_{D1}	Delay Time	Figure 1 (4803)	—	160	200	ns
t_{D2}	Delay Time	Figure 1 (4803)	—	200	500	ns
t_{D1}	Delay Time	Figure 1 (4804)	—	160	200	ns
t_{D2}	Delay Time	Figure 1 (4804)	—	240	500	ns
F_{MAX}	Maximum Operating Frequency		1	1.2	—	MHz
Power Supply						
I_{DD}	Power Supply Current	Output HIGH	—	4.0	8.0	mA
I_{DD}	Power Supply Current	Output LOW	—	3.0	5.0	mA
V_S	Start up threshold		—	8.7	10.0	V
V_{UV}	Drop-out threshold		7.5	8.4	—	V

NOTE: 1. 1µs, 1% duty cycle pulse input, output shorted to V_{DD} or GND.

PIN CONFIGURATION



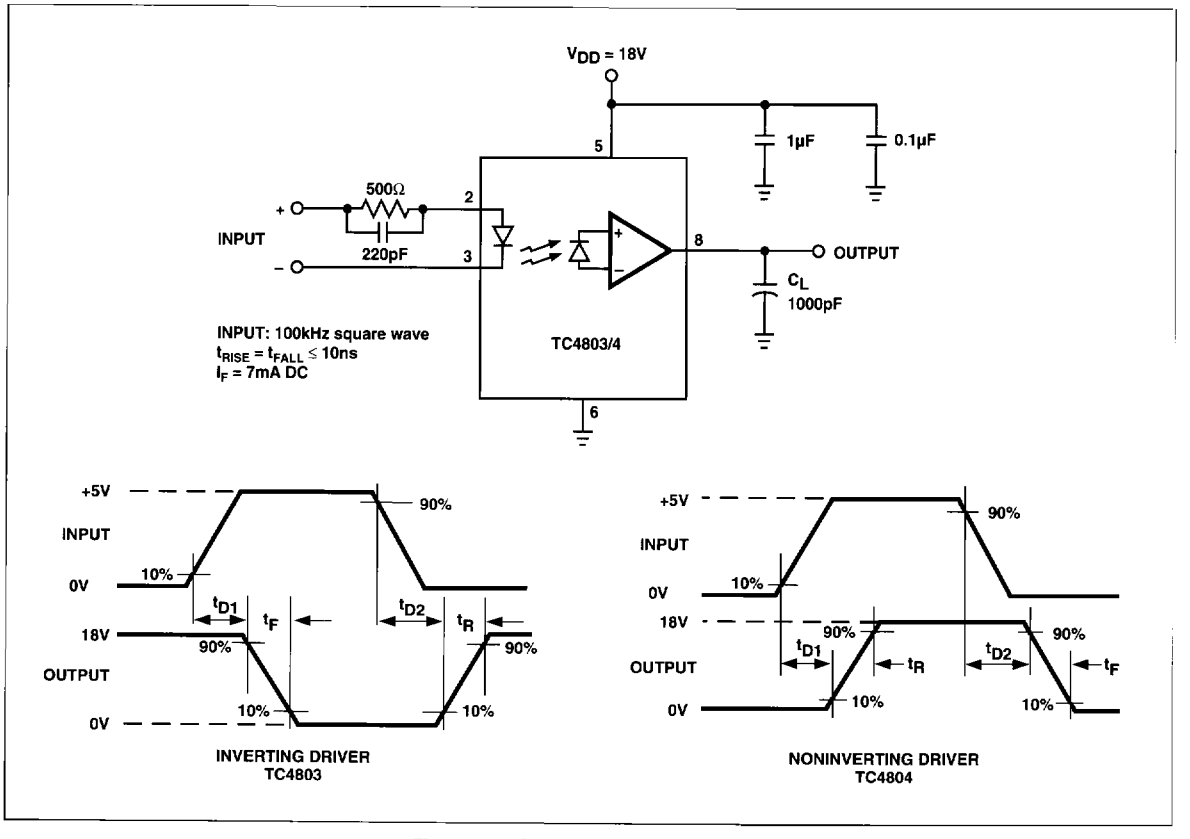


Figure 1 Switching Time Test Circuit

TYPICAL CHARACTERISTIC CURVES